COMMISSION OF THE EUROPEAN COMMUNITIES

COM(94) 450 final

Brussels, 27.10.1994

Proposal for a

COUNCIL REGULATION (EC)

temporarily suspending the autonomous Common Customs Tariff
duty on certain industrial products
(in the microelectronics and related sectors)

(presented by the Commission)

EXPLANATORY MEMORANDUN

- 1. In the third quarter of this year, the Commission, with the assistance of the Economic Tariff Questions Group, examined all requests, submitted by Member States, for temporary suspension of common customs tariff duties, including the requests for prolongation of the suspensions currently in force.
- 2. The attached proposal concerns certain industrial products in the microelectronics and related sectors.

The proposals for Regulations temporarily suspending the autonomous commun customs tariff duties on agricultural products and on other industrial products, will be presented later to the Council.

- 3. Requests for suspension relating to the above products were examined in the light of the criteria defined in the Commission communication to the Council and the Member states, concerning autonomous tariff suspensions (cf. OJ n° C 235 of 13.9.89, p. 2).
 - As a result of this examination, the Commissions deems it justificable to suspends the duty on those products listed in the Annex to the attached draft Regulation.
- 4. As stated in Article 1 of the aforementioned Regulation, the proposed period of validity is 6 months.

Proposal of a

COUNCIL REGULATION (EC) No of 1994

/94

temporarily suspending the autonomous Common Customs Tariff duty on certain industrial products (in the microelectronics and related sectors)

THE COUNCIL OF THE EUROPEAN UNION.

Having regard to the Treaty establishing the European Community, and in particular Article 28 thereof.

Having regard to the proposal from the Commission,

Whereas production of the products referred to in this Regulation is at present inadequate or non-existent within the Community and producers are thus unable to meet the needs of user industries in the Community;

Whereas it is in the Community's interest in certain cases to suspend the autonomous Common Customs Tariff duties only partially, particularly because of the existence of Community production, and in other cases to suspend them completely;

Whereas suspension of these autonomous duties shall be decided by the Community;

Whereas, taking account of the difficulties involved in accurately assessing the development of the economic situation in the sectors concerned in the near future, these suspension measures should be taken only temporarily, by fixing their period of validity by reference to the interests of Community production,

HAS ADOPTED THIS REGULATION:

Article 1

The autonomous Common Customs Tariff duties for the products listed in the table appearing in the Annex shall be suspended at the level indicated in respect of each of them.

These suspensions shall apply from 1 January to 30 June 1995.

Article 2

This Regulation shall enter into force on 1 January 1995.

This Regulation shall be binding in its entirety and directly applicable in all Member States.

Done at

1994.

For the Council

The President

ANNEX TABLE

		IADLE	A contract of the contract of
CN code .	TARIC	Description	Rate of autonomous duty (X)
1 ex84719286	*10	Input unit (so-catted "touchpad"), the exterior dimensions of which do not exceed 50 x 62 mm, capable of matrix scanning and detection, consisting of 2 tayers of measurement electrodes, a printed circuit, a capacitive matrix, 2 integrated circuits, discrete components and a connector, for use in the manufacture of creducts follows and in the manufacture.	
		of products falling within heading 8471 (a)	8
2 ex84719988	*18	Optical reader for reading alphanumeric dot-matrîx printing characters and converting them into electrical signals, comprising a read head containing an optical detector, an amplifier, a focusing tens and two lamps, linked by one or two flat cables to a central module the dimensions of which do not exceed 200 x 220 mm, comprising a printed circuit board on which are mounted a microprocessor, an image recognition circuit and an analogue-to-digital converter	θ
6 ex84733010	*15	Processor, consisting of:	
		 15 monolithic integrated circuits, comprising an arithmetic-logic unit (ALU) of 32 bits, a halfword arithmetic-logic unit (ALU), a halfword multiplier, a floating point unit, a fixed point unit, a storage control unit, a storage interface circuit and 10 static random-access memories (S-RAMs) with a total storage capacity of 5760 Kbits, decoupling capacitors and cooling plates, 	•
		the whole mounted on a multilayer ceramic substrate the exterior dimensions of which do not exceed 65 x 65 mm, with not more than 624 connections and bearing:	
		 an identification marking consisting of or including one of the following combinations of figures and letters: 	
		1667559 1667629	• • • • • • • • • • • • • • • • • • • •
		07	•
	·	 other identification markings relating to devices complying with the abovementioned description 	0
7 ex84733010	≢ 25	Processor, consisting of: - 12 monolithic integrated circuits, comprising 2 central processing units (CPUs) with an integer/floating point unit, 2 cache controllers, memory management and tag units (CMTUs) and 8 static random-access memories (S-RAMs) with a total storage capacity of 4 Mbits, - decoupling capacitors and cooling plates,	· ·
	•	the whole mounted on a multilayer ceramic substrate the exterior dimensions of which do not exceed 84 x 147 mm, with not more than 188 connections and bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		RT 6626K RT 6236K	•
. •		or 1	
		 other identification markings relating to devices complying with the abovementioned description 	e
3 ex84733010	*35	Processing system, consisting of: - not more than 121 monolithic integrated circuits not contained in a housing (chip), - a ceramic substrate,	
		the whole enclosed between a metallic baseplate and a metallic plate incorporating not more than 121 cooling pistons <u>filled</u> with liquid	8
-4 ex84733018	145	Memory module, consisting of <u>at least</u> 2 read only memories, non-programmable (ROMs)	8
8 ex84733010	•50	Assembly for disc storage units of Winchester technology, comprising a 2- or 4-channel read/write monolithic integrated circuit for magnetic head signals mounted with discrete components on a flexible printed circuit	- -
		contained on a receiped between cricore	

	CN code	TARIC	Description	Rate of autonomous duty (%)
119	ex84733010	*55	Flash electrically erasable, programmable, read only memory (Flash-E ² PROM) with a storage capacity of 32 Mbits, consisting of 2 monolithic integraled circuits contained in a housing bearing: - an identification marking consisting of or including the	
			following combination of figures and letters:	
•			28 F 032SA	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	в
11	ex84733090	*50	Magnetic bubble memories with a storage capacity of not more than 4 Mbits, contained in a housing the exterior dimensions of which do not exceed 43 x 44 mm, with not more than 56 connections and bearing: - an identification marking consisting of or including one of the following combinations of figures or figures and letters:	
			7110 FBM 54 DB BDL 8133 BDN 8151 MBM 2811 7114-1 FBM 64 DA BDL 8134 BDN 515 MBM 2256	•
			or	
			 other identification markings relating to devices complying with the abovementioned description 	е
12	ex84733090	* 65	Drive-unit, not incorporating a recording drum, for use in the manufacture of digital audio tape storage units (a)	9
13	ex84733090	₹70	Data storage assembly (Head/Disc/Assembly) for disc storage units of the 10,8 inch type, with a data transfer rate per	
			second of 3,9 megabytes, comprising 16 read/write heads and 9 rigid magnetic discs with a total storage capacity, formatted, of 17 gigabytes, the whole incorporated in a single hermetically sealed housing	0
15	ex84734090	* 85	Thermal printer head of thin-film technology, the exterior dimensions of which do not exceed 18 x 98 x 275 mm,	
•	•		consisting of: - a printed circuit on a ceramic substrate fitted with monolithic integrated circuits and 2000 heater elements, - a printed circuit fitted with monolithic integrated circuits, capacitors, resistors and connectors, - a thermistor and	
	- AF011000	*50	- 1 or 2 cooling plates	
	ex85611699	.#58	DC motor, with brushes, with a diameter of 6 mm (±0,2 mm) oder 7,1 mm (±0,2 mm), a shaft of a diameter of 1 mm (±0,82 mm), a rated speed of 6000 (±24 %) rpm and a supply voltage of 1,25 V (±36 %)	.
17	ex85011099	≠76	DC motor, coreless, with a diameter of 18 mm (±0,1 mm), a shaft with a diameter of 0,8 mm (±0,02 mm), a rated speed of 5000 (±20 %) rpm and a supply voltage of 1,25 V (±36 %)	8
20	ex85045090	•10	Inductor_with a variable <u>inductance</u> not exceeding 62 aH	9
18	ex85045090	≇28	Multilayer monolithic inductors, with an inductance of 0,038 wh or more but not exceeding 40 mH, contained in a housing of the SMD (Surface mounted device) type, for use in the manufacture of pagers (a)	
23bis	ex85073091	\$10	Rectangular nickel-cadmium accumulator, having a nominal capacity of 1200 mAh and a nominal voltage of 1,2 V, with a length of 67 mm (±0,1 mm), a width of 16,8 mm (±0,05 mm) and a	
		, -	thickness of 10,5 am (±0,05 mm), for use in the manufacture of rechargeable batteries (a)	
23	ex85079098	*16	Rectangular nicket-hydrid accumulator, with a length of 48,4 mm (±1 mm), a width of 14,5 mm (±1 mm) and a thickness of 7,5 mm (±1 mm), avoing a capacity of 500 mAh or more and a nominal voltage of 1,2 V, for use in the manufacture of rechargeable batteries (a)	
			batter <u>ies</u> (a)	θ

	CN code	TARIC	Description	Rate of autonomous duty (x)
24	ex85178298	118	Transmitter, capable of converting electrical signals into light pulses, operating at a nominal wavelength of 850 nm, comprising a light-emitting diode (LED), a current switch, an input buffer and a distortion/compensation circuit, contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			DH-231-TA	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	6
25	ex85178290 .	•28	Receive unit, capable of converting light pulses into electrical signals, operating at a nominal wavelength of 850 nm, comprising a photodiode, 2 decision circuits, an amplifier and an integrator, contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
	÷		DH-231-RA	
			or	
	·		 other identification markings relating to devices complying with the abovementioned description 	θ · ·
26	ex85179011	∎0t	Modulator/demodulator of C-MOS technology (C-MOS-Modem), for full duplex data-transfer at a rate of 28808 bits per second and for half duplex transfer of image telegraphy (facsimite) at a rate of 14400 bits per second, only consisting of 2 or more monolithic integrated circuits, one of which for digital signal processing (DSP) and an other for analogue functions, counted on a printed circuit, contained in a housing bearing: - an identification marking consisting of or including one of	
		•	the following combinations of figures and letters: RC.192DP RC 192DPL RC 248DP RC 248DPL RC 288DP	•
			or ·	
			- other identification markings retating to devices complying with the abovementioned description	0
276	ex85179011	•82	Modulator/demodulator of C-MOS technology (C-MOS-Modem), only for half duplex transfer of data or image telegraphy (facsimite) at a rate not exceeding 2400 bits per second, only consisting of 2 monotithic integrated circuits, one of which for digital signal processing (DSP) and the other for analogue functions, mounted on a printed circuit, contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			RC 24BKJ	-
			01	
		•	 other identification markings relating to devices complying with the abovementioned description 	9 .
27c	ex85179011	+63	Modulator/demodulator of C-MOS technology (C-MOS-Modem), only for full duplex data-transfer at a rate not exceeding 2400 bits per second, only consisting of 2 monotithic integrated circuits, one of which for digital signal processing (DSP) and the other for analogue functions, mounted on a printed circuit, contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	. · · · · · · · · · · · · · · · · · · ·
			RC 2324DPL	-
· - .			or the second of	
			 other identification markings relating to devices complying with the abovementioned description 	θ

	And the second s	
CN code TARIC	Description	Rate of autonomous duty (X)
28 ex85179011 +84	Modulator/demodulator of C-MOS technology (C-MOS-Modem), only for half duplex transfer of image telegraphy (facsimite) at a rate not exceeding 9600 bits per second, only consisting of 2 monolithic integrated circuits, one of which for digital signal processing (DSP) and the other for analogue functions, mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters:	
	R 96DFX R 96EFX R 96MFX	
	 other identification markings relating to devices complying with the abovementioned description 	0
27a ex85179819 •28	Modulator/demodulator of C-MOS technology (C-MOS-Modes), for full duplex data-transfer at a rate not exceeding 9600 bits per second and for half duplex transfer of image telegraphy (facsimile) at a rate not exceeding 9800 bits per second, only consisting of 2 or 3 monolithic integrated circuits, 1 or 2 of which for digital signal processing (DSP) and an other for analogue functions, mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and tetters:	
	RC 9323 RC 9624 RC 96DPL RC 96V24	•
	or	
	- other identification markings relating to devices complying with the abovementioned description	
33 ex85179081 +38	Assembly consisting of a laser diode operating at a mominal wavelength of 780 nm, a photodiode and a lens, contained in a housing with a diameter of not more than 9 mm and a height of not more than 20 mm, with not more than 3 connections and bearing: - an identification marking consisting of or including one of the following combination of latters or of figures and latters:	
	FU-0118LD-N2 LM-7115	
	or	
	- other identification markings relating to devices complying with the abovementioned description	8
34 ex85229091 #91	Optical unit consisting of a laser diods with one photodiods, emitting light of a mominal wavelength of 780 nm, contained in a housing with a dismeter of not more than 10 mm and a height of not more than 9 mm, with not more than 10 connections and bearing: - an identification marking consisting of or including one of the following combination of tetters or of figures and letters:	
	LDGU LT 022	
	or .	
	- other identification markings relating to devices complying with the abovementioned description	9
35 ex85229091 +92	Electronic assembly for a laser read-head of a compact disc player, comprising: - a flexible printed circuit, - a photo-detector, in the form of a monolithic integrated circuit, contained in a housing, - 2 compactors	•
 .	- 2 connectors, - not more than 1 transistor, - not more than 3 variable and 4 fixed resistors, - not more than 5 capacitors, the whole mounted on a support	8

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	CN code	TARIC	Description	Rate of autonomous duty (%)
35bis	ex85229099	•92	Thinfilm recording <u>and</u> reproducing device, having 9 parallel channels for digital signals and 2 channels for analogue signals, to which a non-magnetic ceramic substrate is fixed, the whole rounded at one side, for use in the manufacture of magnetic heads for digital sound recording and digital/analogue sound reproducing apparatus of the cassette-type (a)	θ
40	ex85229099	*94 *	Sound reproducing assembly, consisting of a compact disc mechanism, comprising an optical reading system and 3 DC motors, for use in the manufacture of products falling within subheading 85272110 (a)	0
36	ex85232010	∍ 50	Rigid magnetic disc with a thin-film metallic coating, having a coercivity exceeding 600 De and an external diameter not exceeding 231 mm	0
37	ex85291070	*10	Ceramic filter package comprising 2 ceramic filters and 1 ceramic resonator for a frequency of 10,7 MHz (±30 kHz), contained in a housing the exterior dimensions of which do not exceed 10 x 10 mm	8
38	ex85291070	\$28	Ceramic filters for frequencies of 4,5 MHz or more but not exceeding 6,6 MHz contained in a housing the dimensions of which do not exceed 9 x 24 mm	θ
41	e×85299098	•31	Demagnetisation coil with not more than 96 windings, with cables and connectors	0
42	ex85299098	*32	Filter, consisting of 2 piezo-electric crystals each with a frequency of 21 MHz or more but not exceeding 30 MHz and seperately mounted on a bracket, with not more than 7 connections	0
43	ex85299098	•96	Assembly comprising a lens unit, having a focal length of 3,6 mm, an interline charge-coupled image sensor having 291000 photosensitive cells, and integrated circuits, the whole mounted on a printed circuit	θ
44	ex85312051	∍ 10	Liquid crystal colour display (LCD) with an active matrix and 640 x 480 pixels, consisting of a layer of liquid crystals between two glass sheets or plates, mounted on a printed circuit board comprising electronic components providing drive and/or control functions	θ
45	ex85312051	*20 ^	Liquid crystal colour display (LCD) with an active matrix and 768 x 1024 pixels, consisting of a layer of liquid crystals between two glass sheets or plates, comprising electronic components providing drive and/or control functions	
46	ex85312059	•10	Liquid crystal monochrome display (LCD) with an active matrix and 640 x 400 or 640 x 480 pixels, consisting of a layer of liquid crystals between two glass sheets or plates, mounted on a printed circuit board comprising electronic components providing drive and/or control functions	0
.47	ex85312059	∗20	Liquid crystal monochrome display (LCD) with an active matrix and 900 x 1152 pixels, consisting of a layer of liquid crystals between two glass sheets or plates, comprising electronic components providing drive and/or control functions	0
49	ex85312080	*10	Liquid crystal display (LCD) with a passive matrix, mounted on a printed circuit board comprising electronic components providing drive and/or control functions	6
51	ex85318090	+20	Transducer, capable of producing a sound level of 85 dB at a frequency of <u>2700 or</u> 3200 Hz	0
52	ex85322200	•95	Atuminium electronic capacitors, with a fixed nominal capacity not exceeding 470 µF and an operating voltage not exceeding 50 V, operating within a temperature range of -40°C to +85°C	6
53	ex85322200	*96	Atuminium eletronic capacitors, with a fixed nominal capacity of 2,2 yF <u>and</u> an operating voltage of 385 V, operating within a temperature range of -40°C to +85°C	- 6

	CN code	TARIC	Description	Rate of autonomous duty (%)
55	ex85331000	* 91	Fixed carbon-film resistor of the SMD (Surface mounted device) type, with a continous operating voltage not exceeding 250 V	0
54	ex85331000	*92	Fixed carbon composition resistor, with an operating voltage not exceeding 350 V and a dissipation rate not exceeding θ,5 W	0
56	ex85340011	*93	Multiple printed circuits on a ceramic substrate, the exterior dimensions of which are not less than 125 x 125 mm and do not exceed 129 x 129 mm, consisting only of conductor elements and 2772 contacts	θ
58	ex85340019	*95	Printed circuit, consisting of conductor elements fixed on a flexible plastic film, with a trace width of 0,095 mm or more but not exceeding 3.5 mm and a trace pitch of 0,095 mm or more but not exceeding 0,305 mm, for use in the manufacture of electronic calculating machines (a)	. в
57	ex85340019	≇96	Printed circuit on an aluminium_oxide support, only with gold plated conductor elements of thick film technology, for use in the manufacture of products falling within subheading 85422050 (a)	8
61	ex85365090	₹92	Reed switch having a switching power of 20 W or more within the range of 17-43 A.turn, in the form of a glass capsule, not containing mercury, the dimensions of which do not exceed 3 x 21 mm, for use in the manufacture of automotive airbag shock-sensors (a)	θ
62	ex85401200	#82	Monochrome cathode-ray tube with a diagonal measurement of the screen of 250 mm or more but not exceeding 320 mm and an anode voltage of 18 kV or more but not exceeding 22 kV	8
63	ex85403010	#34	Colour cathode-ray tube with a dot mask, equipped with 3 electron guns placed side by side (in-line technology) or 1 gun with 3 rays, with a dïagonal measurement of the screen of more than 72 cm and a distance of less than 0,5 mm between dots of the same colour	8
64	ex85493010	* 35	Colour cathode-ray tube with a dot mask, equipped with 3 electron guns placed side by side (in-line technology) or 1 gun with 3 rays, having a diagonal measurement of the screen not exceeding 72 cm	θ
65	ex85409100	#94	Deflector yoke for a colour cathode-ray tube, with an operating frequency of 15625 or 31250 Hz, comprising two 2-pole ring magnets, two 4-pole ring magnets and two 6-pole ring magnets	θ
66	ex85409100	∌ 96	Assembly for a cathode-ray tube with 2 or more but not more than 6 coils, a platic support and a metal fixing ring, for the adjustment of display sharpness and/or convergence	0
67	ex85409900	*91	Anode, cathode or output part, or an assembly comprising these components (magnetron core tube), for the manufacture of magnetrons of subheading 85404100 (a)	θ
79	ex85411091	*19	. Silicon power rectifier diodes of planar technology, with a recovery time of less than 100 ns, a maximum recurring reverse voltage of 200 V, and average forward current of 2,5 A or more, contained in a flat housing the exterior dimensions of which exceed 3 x 9 x 9 mm but do not exceed 5 x 11 x 17 mm	0
68	ex85411091	•20	Silicon power rectifier diode, with a reverse peak voltage not exceeding 1500 V and an average output current of 8 A, contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
-			PG151815	
		•	or - other identification markings relating to devices complying	

	CN code	TARIC	Description .	Rate of autonomous duty (%)
72	ex85411099	*30	Current regulative diode <u>, providing a stabilized current level</u> not exceeding 18 mA at a voltage of 10 V	0
75	ex85412990	*10	Transistor with a power of 150 W or more at a voltage of 160 V or more and with a cut-off frequency of 20 MHz or more, contained in a housing the exterior dimensions of which do not exceed 22 x 37 mm, with not more than 3 connections and bearing:	
			- an identification marking consisting of or including one of the following combinations of figures and letters:	
			2 SA 1170 2 SA 1494 2 SC 2921 2 SA 1215 2 SC 2774 2 SC 3858	
			or	
	,		 other identification markings retating to devices complying with the abovementioned description. 	θ
77	ex85412990	*30	Field-effect transistor (FET), having a drain-to-source breakdown-voltage not exceeding 60 V, operating with a drain-current not exceeding 8,5 A, a drain-to-source resistance not exceeding 0,3 ohm, and with a dissipation rate not exceeding	
			30 M, contained in a housing the exterior dimensions of which do not exceed 6 x 7 mm, with not more than 3 connections and bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			IRFR 010 IRFR 014 IRFU 014 IRLR 014 IRLU 014	
			or .	•
		÷	 other identification markings relating to devices complying with the abovementioned description 	6
78	ex85412990	*40	Transistor with thermal overload protection, having a collector-emitter operating voltage not exceeding 42 V, contained in a housing with not more than 4 connections	0
79	ex85412990	* 50	Transistor with an output power not exceeding 30 W at a voltage of 12,5 V, contained in a housing with not more than 8 connections	θ
. 74	ex85412990	*69	Field-effect transistor of N-MOS technology (MOSFET), having a drain-to-source breakdown-voltage of 60 V or more, operating with a continuous drain current not exceeding 35 A, a	
			drain-to-source resistance not exceeding 0,028 Ohm and with a dissipation rate not exceeding 125 W, contained in a housing bearing:	
			 an identification marking consisting of or including the following combination of figures and letters: 	•
			51016K	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	θ
73	ex85412990	*7 0	Transistor, having an output power not exceeding 250 W, a base collector-base breakdown voltage of 1500 V or more and a peak collector current not exceeding 40 A, contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
, -			29C3997	
,			or	•
		•	 other identification markings retating to devices complying with the abovementioned description 	- 0

	CN code	TARIC	Description	Rate	of autoi	nomous	duty	(x)
80	ex85414010	* 60	Light-emitting diode (LED), made from gallium-arsenic-phosphor (GaAsP) semiconductor, operating at a nominal wavelength of 710 nanometers, in the form of a monolithic integrated circuit not contained in a housing (chip), for the manufacture of optocouplers (a)	θ				
81	ex85416000	*93	Piezo-electric crystal <u>, excluding surface acoustic wave</u> <u>fitters</u> , oscillating at a frequency of 4 MHz or more but not exceeding <u>155</u> MHz, contained in a housing of the SMD (Surface mounted device) type and with not more than 4 connections	θ				-
82	ex85421101	*12	Wafer, not yet cut into chips, consisting only of microcontrollers or microcomputers of C-MOS technology, with a processing capacity of 16 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 48 Kbits, a read only memory, non-programmable (ROM) with a storage capacity of 16 Kbits and a random-access memory (RAM) with a storage capacity of 4 Kbits, for use in the manufacture of goods of subheading 85421173 contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:					
			77C25					
			or					
			 other identification markings relating to devices complying with the abovementioned description (a) 	θ				
84	ex85421101	#2 5	Wafer, not yet cut into chips, consisting only of microcontrollers or microcomputers of C-MOS or N-MOS (including H-MOS) technology, with a processing capacity of 16 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 510 x 13 bits, a read only memory, non-programmable (ROM) with a storage capacity of 512 x 23 bits and a random-access memory (RAM) with a storage capacity of 2 Kbits, for use in the manufacture of goods of subheading 85421173 contained in a housing bearing: - an identification marking consisting of or including one of			•	•	•
			the following combinations figures or of figures and letters:		•			•
•			7720 77020					
	• •		or - other identification markings relating to devices complying with the abovementioned description (a)	0	•	,		
85	ex85421181	949	Wafer, not yet cut into chips, consisting onty of microprocessors with a processing capacity of 16 bits, for use in the manufacture of goods of subheading 85421164 contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	,	**************************************	· · · · ·		
		•	7810 78C10					
			or					•
-	· .		 other identification markings relating to devices complying with the abovementioned description (a) 	ч. Ө	•			
86	ex85421105	*15	Control and interface circuit of BiMOS technology, capable of controlling communication between a microprocessor, bus control circuits and a memory control circuit, in the form of a monotithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 85421184 contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:					
			1667432 1667433 5066759 5066761	_				:
1			or					
			 other identification markings relating to devices complying with the abovementioned description (a) 	θ				

	CN code	TARIC	Description	Rate of autonomous duty (%)
87	ex85421105	*2 5	Bus control circuit of BiMOS technology, in the form of a monotithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 85421182 contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			1667429 1667430 5066755 5066757	
			or	
			 other identification markings relating to devices complying with the abovementioned description (a) 	0 :
88	ex85421105	• 35	Memory control circuit of BiMOS technology, in the form of a monotithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 <u>2080</u> contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			1607428 1807463	
	•		Or	
			 other identification markings relating to devices complying with the abovementioned description (a) 	0
91	ex85421185	*4 \$	Bus control circuit of C-MOS technology, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 85421182 contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures or of figures and letters:	
		•	2782654 5267386 63F4073 63F4170 69G1705 5267385 63F4057 63F4074 63F4378 81889051	
		* * *	or ,	•,
			 other identification markings relating to devices complying with the abovementioned description (a) 	6
97	ex85421105	+ 50	Microprocessor with a processing capacity of more than 16 bits, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 85421167 or 85421168 contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures or of figures and letters:	
	•		3267577 80501 8186364 8188939 82F5700 PC 603 3267578 8186362 8188937 82F5698 PC 601 PC 604	
			or .	
		•	 other identification markings relating to devices complying with the abovementioned description (a) 	0
92	ex85421105	#55	Memory control circuit of C-MOS technology, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 85421182 contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures or of figures and letters:	
		ŕ	3267468 3267567 5066870 5068191 8186387 8188905	•
		. •	or	-
· .			 other identification markings retating to devices complying with the abovementioned description (a) 	8

	CN code	TARIC	Description	Rate of autonomous duty (%)
93	ex85421105	•68	Triple digital-to-analogue <u>video</u> converter with 3 random-access memories (RAMDACs) of C-MOS technology, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 85421186 contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of <u>figures or of</u> figures and letters:	
			RGB525 RGB530 (8187135) RGB528 RGB561 (8186987)	
			or ,·	•
			 other identification markings relating to devices complying with the abovementioned description (a) 	0
94	ex85421105	•65	Bus interface and control circuit of C-MOS technology, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 85421184 contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			2782454	
			or	
			 other identification markings relating to devices complying with the abovementioned description (a) 	θ
96	ex85421105	* 78	Data/address buffer circuit of C-MOS technology, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 854211 <u>86</u> contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures:	•
			2782653 8198694	• .
			OF	
			 other identification markings relating to devices complying with the abovementioned description (a) 	0
98	ex85421114	* 82	Dual or triple port dynamic random-access memory (D-RAM), with data registers and a serial read output control, with a storage capacity exceeding 256 Kbit but not exceeding 1 Mbit, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
	. •		M5H 442256	
			or	
	•		 other identification markings relating to devices complying with the abovementioned description 	6
99	ex85421114	* 03	Random-access memory, with separate in- and outputs and serial shift registers (so-called field memories), of C-MOS technology, with a storage capacity of 870 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	•
			TMS 4C1081	•
	•	•	or	-
- .			 other identification markings relating to devices complying with the abovementioned description 	9

	CN code	TARIC	Description	Rate of autonomous duty (%)
100	ex85421116	•05	Dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a storage capacity of 512 K x 8 bits and an access time not exceeding 35 ns, comprising 4 static random-access cache memories (S-Cache-RAMs) with a total storage capacity of 8 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			DM 2203 DM 2213	
	•		or	
			 other identification markings relating to devices complying with the abovementioned description 	0
101	ex85421116	*06	Dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a storage capacity of 2 Mbits and an access time not exceeding 60 ns, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
	4	•	V53C8256	. •
			Or	
			 other identification markings relating to devices complying with the abovementioned description 	0
102	ex85421116	* 67	Random-access memory, with separate in- and outputs and serial shift registers (so-called field memories), of C-MOS technology, with a storage capacity of 1920 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		\$	TMS 4C2070	
			or	•
	·		 other identification markings relating to devices complying with the abovementioned description 	0
103	ex85421116	´∗08	Dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a storage capacity of 512 K x 8 bits and an access time not exceeding 100 ns, operating with a supply voltage of 3,3 V (±0,3 V), in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	_
			HM51W4800 (74G1307) (70G6821)	
			or	
	·		 other identification markings relating to devices complying with the abovementioned description 	0
105	ex85421118	* 03	Random-access memory, with separate in- and outputs and serial shift registers (so-called field memories), of C-MOS technology, with a storage capacity of 4320 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			TMS 53805	
~		•	or	
			 other identification markings relating to devices complying with the abovementioned description 	. 0
197	ex85421121	#19	Quadruple port static random-access memory of C-MOS technology (C-MOS quadruple port S-RAM), with a storage capacity not exceeding 64 Kbits and an access time not exceeding 45 ns, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	

	CN code	TARIC	Description	Rate of autonomous duty (%)
			107 2050 107 2050	•
			1DT 7050 IDT 7052	
			or	. = %
			 other identification markings relating to devices complying with the abovementioned description 	: '8
228	ex85421121	*20	Static random-access cache memory of <u>C-MOS</u> technology (<u>C-MOS</u> S-Cache-RAM), with a storage capacity of <u>16</u> Kbits, comprising an <u>4</u> -bit <u>identity</u> comparator, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the	
			• following combination of figures and letters:	
			MCM 62350	
			or	
ē.			 other identification markings relating to devices complying with the abovementioned description 	
109	ex85421121	\$51	Static random-access memory of C-MOS technology (C-MOS 8-RAM), with a storage capacity of 64 Kbits and an access time exceeding 55 ns, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of	•
	•		the following combinations of figures and letters:	•
			HV 6264A TC 5564	
			or	
			 other identification markings relating to devices comptying with the abovementioned description 	0
110	ex85421123	*03	Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 192 Kbits and an access time not exceeding 35 ns, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			NCM 56824	. •
	•		or	
			 other identification markings relating to devices complying with the abovementioned description 	8
112	ex85421125	*10	Static random-access memory (S-RAM), with a storage capacity of 1 Mbit, in the form of a monolithic integrated circuit contains	
	÷		in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		•	CXXS81020 CY7C109 MSM 51004 TC 55B4256 CY7C101 EDI 88128 M5M 51008 TC 55B4257 CY7C102 HM 621100A MCM 6228 TC 55B8128 CY7C106 HM 624256 MCM 6729 CY7C107 HM 624257 MT 5C1008 CY7C108 GM 76C8128 TC 551001	
			or	
			- other identification markings relating to devices complying with the abovementioned description	6
11-3	ex85421125	*13	Static random-access memory of C-MOS technology (C-MOS S-RAM), operating at a power supply of 3,3 V (±0,3 V), with a storage capacity of 288 Kbits and an access time not exceeding 35 ns, the form of a monolithic integrated circuit contained in a	

housing bearing:
- an identification marking consisting of or including the following combination of figures and letters:

CV7C1388

- other identification markings relating to devices complying

	CN_code	TARIC	Description	· Rate of	autonomous	duty ((X)
	١.		with the abovementioned description	0			
115	ex85421142 ex85421159	*21 *61	UV erasable or non-erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 256 Kbits and an access time <u>not exceeding 65</u> ns, in the form of a monolithic integrated circuit contained in a housing, with or without a quartz window on the upper surface, and bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:		,	•	
			27C256-55 CV 7C 278 CV 7C 275 CV 7C 279 27H256-35 CV 7C 271 CV 7C 276 XC 17256D 27H246-45 CV 7C 274 CV 7C 277				
			or				
			 other identification markings relating to devices complying with the abovementioned description 	0			•
116bis	ex85421144	≱ 06	Flash electrically erasable, programmable, read only memory (Flash-E ² PROM) with a storage capacity of 1 Mbit, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:		•	•	
			28 F 010 28 F 210 29 F 010 29 F 100 48 F 010				
			or .				
	•		 other identification markings relating to devices complying with the abovementioned description 	в			
116	ex85421144 ex85421159	*07 *68	UV erasable or non-erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 512 Kbits and an access time not exceeding 55 ns, in the form of a monolithic integrated circuit contained in a housing, with or without a quartz window on the upper surface, and bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:				
			CV 7C 286 <u>-50</u> CV 7C 287 <u>-45</u> CV 7C 287 <u>-55</u>				
			or				
	•		 other identification markings relating to devices complying with the abovementioned description 	. 0			
114	ex85421144	#08	Flash etectrically erasable, programmable, read only memory (Flash-E ² PROM) with a storage capacity of more than 256 Kbits but not exceeding 896 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:				
	•		N71564FTCHCA				
	,		or	•			
	:	•	- other identification markings relating to devices complying with the abovementioned description	8			
117	ex85421146	*01	Flash electrically erasable, programmable, read only memory (Flash-E ² PROM) with a storage capacity of 2 Mbits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		
-			28 F 002 28 F 020 28 F 200 <u>29 F 200</u>				
			or	٠.			
· <u>.</u> .			 other identification markings retating to devices complying with the abovementioned description 	- 0	<u>-</u>		,

CN code	TARIC	Description	Rate of autonomous duty (%)
118 ex85421146	# 02 _	Flash electrically erasable, programmable, read only memory (Flash-E ² PROM) with a storage capacity of 4 Mbits, in the form of a monolithic integrated circuit contained in a housing bearing:	
	•	 an identification marking consisting of or including one of the following combinations of figures and letters: 	
		28 F 040 TC 584000	
		or	
		 other identification markings relating to devices complying with the abovementioned description 	8
122 ex85421148	* 02	Flash electrically erasable, programmable, read only memory (Flash-E ² PROM) with a storage capacity of 8 Mbits, in the form of a monolithic integrated circuit contained in a housing	
		 bearing: an identification marking consisting of or including the following combination of figures and letters: 	
•		28 F 008	
		or	•
		 other identification markings relating to devices complying with the abovementioned description 	0
120 ex85421148	*03	Flash electrically erasable, programmable, read only memory (Flash-E ² PROM) with a storage capacity of 16 Mbits, in the form of a monolithic integrated circuit contained in a housing bearing:	
	٠.	 an identification marking consisting of or including the following combination of figures and letters: 	
	•	28 F 016SA	•
	•	or	•
		- other identification markings relating to devices complying with the abovementioned description	0
125 ex85421150	* 86	Electrically erasable, programmable, read only memory (E ² PROM), with a storage capacity of 8 K x 8 bits, in the form of a monolithic integrated circuit contained in a housing bearing:	
		 an identification marking consisting of or including one of the following combinations of figures and letters: 	
		AT 28 C 64 AT 28 HC 64 <u>EX 28 C 64</u> AT 28 <u>H</u> 64 AT 28 PC 64	
	,	or	,
		 other identification markings relating to devices complying with the abovementioned description 	0
106 ex85421159	# 82	Ferroelectric memory of C-MOS technology, in the form of a monotithic integrated circuit contained in a housing bearing:	
		- an identification marking consisting of or including the following combination of figures and letters:	
		FM1208S	
		or	
		 other identification markings relating to devices complying with the abovementioned description 	0
126 ex85421159 .	∗ 07	FIFO (first in, first out) read/write memory of C-MOS technology, for simultaneous reading and writing in one clock signal, with a storage capacity of 2 K x 18 bits, in the form of a monolithic integrated circuit contained in a housing	· •
	•	 bearing: an identification marking consisting of or including one of the following combinations of figures and letters: 	
·		CY7C447 CY7C457 HT 53C2K18C3	

- althor identification surtings relating to devices complying with the absorberationed description 127 cxx85421159 122 FFFC (first in, first out), rest/rrite meansy of C-RB technology, with a cirrage appelling of 8 x 8 3 5 is or 6 x 9 biles, in the few of a sensitivit integrated circuit contained an intention serving containing and the fallowing combinations of figures and tetters: CYTC4898 CYTC4898 or - althor identification servings relating to devices complying with the absorberationed description with the absorberationed description and tetters: - an identification serving constituting of an including one of the fallowing combinations of figures and tetters: - an identification serving comply and tetters are also serving and tetters: - an identification serving comply and tetters: - an identification serving relating to devices complying with the absorberations of figures and letters: - an identification serving complying and tetters: - an identification serving relating to devices complying with the absorberations of figures and letters: - an identification serving complying the devices complying with the absorberations of description - althor identification serving complying and integrated and relating to the fallowing complying with the absorberations of figures and tetters: - CYTC441 CYTC631 - althorized complying and tetters: - CYTC442 CYTC631 - althorized complying and tetters: - CYTC443 CYTC631 - althorized complying and integrate complying with the absorberationed description of figures and tetters: - CYTC443 CYTC631 - althorized complying and selection serving complying in a following combination of figures and tetters: - CYTC443 CYTC631 - althorized complying and integration of principle devices complying with the absorberationed description of figures and tetters: - CYTC443 CYTC631 - althorized complying and integrated creating containing in a notation of figures and tetters: - CYTC443 CYTC631 - althorized complying and integrated creating containing in a notation becom	•••			
### with the abovement/mosed description 0	CN code	TARIC	Description	Rate of autonomous duty (%)
technology, with a storage capacity of 64 x 6 bits or 6 x 9 bits, in the fore of a sanothic integrated circuit contained in a housing bearing: - an identification warking consisting of or including one of the fictioning continuing continuing of figures and testions. - CVTCEGAR - CVTCEGAR - CVTCEGAR - or - other identification swrkings relating to devices complying with the abovementioned description 128 ex85421159 124 - FIPO (first in, First old) read/write among with a storage capacity of capacity of the first old				0
the following combinations of figures and testers: CVTC4888 CVTC4888 or - other identification markings relating to devices complying with the abovementioned description 128 ex85421159 124 FIFO first, in, first out) read/rate exerts, with a strange capacity of 7280 or 9888 bits, in the form of a manifility integrated circuit contained in a housing bearing: - an identification markings consisting of or including one of the fallowing combination of figures and testers: PD 41101 PD 41102 PD 42101 PD 42102 or - other identification markings relating to devices complying with the abovementioned description 129 ex85421159 126 FIFO (first in, first out) read/rate searcy of C-MOS technology, for similar contained in a housing bearing; and an access time not acceding 40 ms, in the form of a monocithic integrated circuit contained in a housing bearing; of an including one of the following combinations of figures and testers: CVTC461 CVTC451 or - other identification markings relating to devices complying with the abovementioned description 130 ex85421159 127 FIFO (first in, first out) read/ratio exercy capacity of 5 contained in a housing bearing; - an identification markings relating to devices complying with the abovementioned consisting of or including one of the following combinations of figures and testers: CVTC43 CVTC45 NT S2C5121841 CVTC445 CVTC45 NT S2C5121841 CVTC446 CVTC45 NT S2C5121841 CVTC446 CVTC45 NT S2C5121841 - other identification markings relating to devices complying with the abovementioned description 121 ex85421159 128 FIFO (first in, first out) read/rate manage of C-MOS technology, with a storage capacity of 75 this and sitts an access time not acceding 15 an are a programmable figures and testers: CVTC448 CVTC45 NT S2C5121841 - other identification markings relating to devices complying with the abovementioned description or - other identification markings consisting of or including one of the following combinations of figures and testers: CVTC448 TS S2C41880+15 or - othe	127 ex85421159	#21	technology, with a storage capacity of 64 x $ ilde{8}$ bits or 64 x 9 bits, in the form of a monotithic integrated circuit contained in a housing bearing:	
or - ather identification warkings relating to devices complying with the abovementioned description 128 mx85421159 124 FIFO (first in, first out) read/write among, with a storage capacity of 7280 or 9808 bits, in the form of a monithic integrated circuit contained in a housing barring: - an identification warkings consisting of or inctuding one of the following contained on a figure and includes: - p0 41101 P0 41102 P0 42101 P0 42102 or - other identification warkings relating to devices complying with the abovementioned description 128 mx85421159 128 mx85421159 129 mx85421159 120 mx85421159 120 mx85421159 121 mx85421159 122 mx85421159 123 mx85421159 124 mx85421159 125 mx85421159 126 mx85421159 127 mx85421159 128 mx85421159 129 mx85421159 120 mx85421159 120 mx85421159 121 mx85421159 122 mx85421159 123 mx85421159 124 mx85421159 125 mx85421159 126 mx85421159 127 mx85421159 128 mx85421159 129 mx85421159 120 mx85421159 120 mx85421159 121 mx85421159 122 mx85421159 123 mx85421159 124 mx85421159 125 mx85421159 126 mx85421159 127 mx85421159 128 mx85421159 129 mx85421159 120 mx85421159 120 mx85421159 121 mx85421159 122 mx85421159 123 mx85421159 124 mx85421159 125 mx85421159 126 mx85421159 127 mx85421159 128 mx85421159 129 mx85421159 120 mx85421159 120 mx85421159 120 mx85421159 121 mx85421159 122 mx85421159 123 mx85421159 124 mx85421159 125 mx85421159 126 mx85421159 127 mx85421159 128 mx85421159 129 mx85421159 120 mx85421159 120 mx85421159 120 mx85421159 121 mx85421159 122 mx85421159 123 mx85421159 124 mx85421159 125 mx85421159 127 mx85421159 128 mx85421159 129 mx85421159 120 mx85421				•
- other identification markings relating to devices complying with the abovementioned description 128 mm85421158 124 FIRE (first in, first and D) read/or its memory, with makerage capacity of 7280 or 5880 bits, in the form of a mentihit integrated circuit contained ir an abusing bearing; - an identification marking constating of or including one of the following combinations of figures and letters: DP 41181 PD 41182 PD 42181 PD 42282 or - other identification markings relating to devices complying with the abovementioned description by this made markers tricruit (anniance in a housing bearing; - an identification in a housing bearing; - an identification in a housing bearing; - an identification marking constating of or including one of the following combination of figures and letters: CVTC441 CVTC451 or - other identification markings relating to devices complying with the abovementioned description 130 mm85421159 127 FIPE (first in, first out) read/ortite memory of C-MS technology, for simultaneous reading and writing in one clock signat, with the abovementioned description 131 mm85421159 132 mm85421159 133 mm85421159 134 mm85421159 135 mm85421159 136 mm85421159 137 mm85421159 138 mm85421159 139 mm85421159 130 mm85421159 130 mm85421159 131 mm85421159 132 mm85421159 133 mm85421159 134 mm85421159 135 mm85421159 136 mm85421159 137 mm85421159 138 mm85421159 139 mm85421159 130 mm85421159 130 mm85421159 131 mm85421159 132 mm85421159 133 mm85421159 134 mm85421159 135 mm85421159 135 mm85421159 137 mm85421159 138 mm85421159 139 mm85421159 130 mm85421159 131 mm85421159 132 mm85421159 133 mm85421159 134 mm85421159 135 mm85421159 137 mm85421159 138 mm85421159 139 mm85421159 130 mm85421159 131 mm85421159 132 mm85421159 133 mm85421159 134 mm85421159 135 mm85421159 137 mm85421159 138 mm85421159 139 mm85421159 130 mm85421159 131 mm85421159 132 mm85421159 133 mm85421159 134 mm85421159 135 mm85421159 137 mm85421159 138 mm85421159 139 mm85421159 130 mm85421159		•	CV7C408A CV7C409A	
## with the abovementioned description 128 ##\$5421159 129 FIFO (first in, first out) read/write memory, with a storage capacity of 7200 or 9808 bits, in the form of a sensitivity capacity of 7200 or 9808 bits, in the form of a sensitivity of an identification serving consisting of ar including one of the fallowing coabinations of figures and letters: PO 41181 PO 41102 PO 42181 PO 42182	· ;		or	
capacity of 7280 or 3888 bits, in the fore of a senotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following cabbinations of figures and letters: PD 41181 PD 41182 PD 42181 PD 42182 or - other identification markings relating to devices comptying with the abovementioned description 129 mx85421159 *26 FIFO (first in, first out) read/write memory of C-808 technology, for simultaneous reading and writing in one clock signat, with a storage capacity of 12 % 9 bits and access limm not exceeding 40 on, in the fore of a monitable integrated circuit contained in a housing barring: - an identification marking consisting of or including one of the following cabbinations of figures and telters: - CV7C441 CV7C451 or - other identification markings relating to devices comptying with the abovementioned description 130 mx85421159 *27 FIFO (first in, first out) read/write memory of C-808 technology, for simultaneous reading and writing in one clock such and the following cabbinations of figures and telters: - CV7C441 CV7C451 or - other identification markings relating to devices comptying with the abovement of figures of a montithic integrated circuit contained in a housing barring: - an identification marking consisting of or including one of the following combinations of figures and tetters: - CV7C443 CV7C455 NT S3C51218A1 - CV7C445 CV7C455 NT S3C51218A1 - CV7C446 IDT 72021 or - other identification markings relating to devices comptying with the abovement long IS not or a programmable figure argister, in the form of a montithic integrated circuit contained in a housing barring: - an identification markings consisting of or including one of the following combinations of figures and tetters: - CV7C460-IS NT S2C41804-IS or - other identification markings relating to devices comptying with the abovement long IS not or a programmable figure.				0 .
or - other identification markings relating to devices complying with the abovementioned description 129 ex85421159 126 FFFO (first in, first out) read/write memory of C-ROS technology, for simultaneous reading and writing in one clock signal, with a storage capacity of 512 x 9 bits and an access time not acceeding 60 mt, in the forms of a sometitic integrated control of the following cabinations of figures and tetters: CVYC441 CV7C451 or - other identification markings relating to devices complying with the abovementioned description 130 ex85421159 127 FFFO (first in, first out) read/write memory of C-ROS technology, for simultaneous reading and writing in one clock signal or bidiractional operation, with a storage capacity of or 18 total control operation, with a storage capacity of or 18 total control operation of figures and tetters: CVYC439 CVYC439 CVYC453 IDT 72831 CVYC443 CVYC455 IDT 72831 OVYC443 CVYC455 AT 33C5121841 CVYC445 CVYC456 CVYC45	128 ex85421159	#24	capacity of 7280 or 9080 bits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of	
- other identification markings relating to devices complying with the abovementioned description 129 ex85421159 126 FIFO (first in, first out) read/orite memory of C-MOS technology, for simultaneous reading and writing in one clock signal, with a storage capacity of 512 x B bits and an access time not exceeding 40 ns, in the form of a montithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: CV7C441 CV7C451 or - other identification markings relating to devices complying with the abovementioned description 130 ex85421159 127 FIFO (first in, first out) read/orite memory of C-MOS technology, for simultaneous reading and writing in one clock signat or bidirectional operation, with a storage capacity of 9 or 18 bbits, in the form of a montithic integrated circuit contained in a housing bearing: - an identification markings relating to devices complying with the storage operating consisting of or including one of the following combinations of figures and tetters: - CV7C439 CV7C453 IDT 72031 - CV7C443 CV7C455 IDT 72031 - CV7C443 CV7C455 IDT 72061 - other identification markings relating to devices complying with the abovementioned description 131 ex85421159 131 ex85421159 132 ex85421159 133 ex85421159 134 ex85421159 135 ex85421159 136 ex85421159 137 ex85421159 138 ex85421159 139 ex85421159 130 ex85421159 131 ex85421159 131 ex85421159 132 ex85421159 133 ex85421159 134 ex85421159 135 ex85421159 137 ex85421159 138 ex85421159 139 ex85421159 130 ex85421159 131 ex85421159 131 ex85421159 132 ex85421159 133 ex85421159 134 ex85421159 135 ex85421159 137 ex85421159 138 ex85421159 139 ex85421159 130 ex85421159 131 ex85421159 131 ex85421159 132 ex85421159 133 ex85421159 134 ex85421159 135 ex85421159 137 ex85421159 138 ex85421159 139 ex85421159 130 ex85421159 130 ex85421159 131 ex85421159 131 ex85421159 132 ex85421159 133 ex85421159 134 ex85421159 135 ex854		•	PD 41101 PD 41102 PD 42101 PD 42102	
## with the abovementioned description 0 129 ex85421159 #26 FIFO (first in, first out) read/orite memory of C-MOS technology, for simultaneous reading and writing in one clock signal, with a storage capacity of \$12 × 8 bits and an access time not exceeding 40 ns, in the form of a sonotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: CV7C441 CV7C451 or - other identification markings relating to devices complying with the abovementioned description 0 130 ex85421159 ***27 ***FIFO (first in, first out) read/orite memory of C-MOS technology, for simultaneous reading and writing in one clock signal are bidirectional operation, with a storage capacity of 9 or 18 Kbits, in the form of a moneithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and tetters: - CV7C439 CV7C455 *** NT S3CS121841 CV7C445 CV7C445 CV7C445 CV7C455 *** NT S3CS121841 CV7C445 CV7C445 CV7C445 CV7C455 *** NT S3CS121841 CV7C445 CV7C445 CV7C445 CV7C455 *** NT S3CS121841 CV7C445 CV7C446 *** NT S3CS121841 CV7C456-15 *** NT S3CS121841 CV7C466-15 *** NT S3CS121841 CV7C4786-15 *** NT S3CS121841 CV7C4786-15 *** NT S3CS121841 *** NT		,	or	•
technology, for simultaneous reading and writing in one clock signal, with a storage capacity of 512 x b bits and an access time not exceeding 40 ns, in the fore of a wonolithic integrated circuit contained in a housing bearing: - an identification warking consisting of or including one of the following combinations of figures and letters: CW7C441 CW7C451 or - other identification warkings relating to devices complying with the abovementioned description 130 ex85421159 127 FIFO (first in, first out) read/write memory of C-MOS technology, for simultaneous reading and writing in one clock signat or bidirectional operation, with a storage capacity of 9 or 18 Kbits, in the fore of a wonolithic integrated circuit contained in a housing bearing: - an identification warking consisting of or including one of the following combinations of figures and letters: CY7C439 CY7C439 CY7C430 CY7C431 CY7C443 CY7C453 CY7C445 CY7C445 CY7C445 CY7C445 CY7C445 CY7C456 CY7C446 IDT 72021 or - other identification warkings relating to devices complying with the abovementioned description b was a company of the form of a wonolithic integrated circuit contained in a housing bearing: - an identification warking consisting of or including one of the following combinations of figures and letters: CY7C466-15 CY7C470 MT S3C4418D4-15 or - other identification warkings relating to devices complying - other identification warkings consisting of or including one of the following combinations of figures and letters: CY7C460-15 CY7C470 Alternation warkings consisting to devices complying				8
- other identification markings relating to devices complying with the abovementioned description 130 ex85421159 127 FIFO (first in, first out) read/write memory of C-MOS technology, for simultaneous reading and writing in one clock signal or bidirectional operation, with a storage capacity of 9 or 18 Kbits, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and tetters: CY7C439 CY7C439 CY7C435 CY7C443 CY7C445 CY7C445 CY7C445 CY7C446 CY7C446 CY7C446 CY7C466 TDT 72021 or - other identification markings relating to devices complying with the abovementioned description 8 131 ex85421159 *31 FIFO (first in, first out) read/write memory of C-MOS technology, with a storage capacity of 72 Kbits and with an access time not exceeding 15 ns or a programmable flag register, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and tetters: CY7C468-15 DT 7205 CY7C470 AT S3C4K1804-15 or - other identification markings relating to devices complying	129 ex85421159	*26	technology, for simultaneous reading and writing in one clock signal, with a storage capacity of 512 x 9 bits and an access time not exceeding 40 ns, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of	
- other identification markings relating to devices complying with the abovementioned description 130 ex85421159 27 FIFO (first in, first out) read/write memory of C-MOS technology, for simultaneous reading and writing in one clock signal or bidirectional operation, with a storage capacity of 9 or 18 Kbits, in the fore of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and tetters: CY7C439 CY7C453 IDT 72031 CY7C443 CY7C455 MT 53C51218A1 CY7C444 CY7C455 CY7C446 IDT 72021 or - other identification markings relating to devices complying with the abovementioned description 8 131 ex85421159 231 FIFO (first in, first out) read/write memory of C-MOS technology, with a storage capacity of 72 Kbits and with an access time not exceeding IS ns or a programmable flag register, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: CY7C460-IS IDT 7205 CY7C470 MT 53C4K18D4-IS or - other identification markings relating to devices complying		•	CV7C441 CV7C451	
with the abovementioned description 130 ex85421159 127 FIFO (first in, first out) read/write memory of C-MOS technology, for simultaneous reading and writing in one clock signal or bidirectional operation, with a storage capacity of 9 or 18 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and tetters: CV7C439 CV7C439 CV7C455 CV7C445 CV7C445 CV7C446 CV7C446 IDT 72021 or - other identification markings relating to devices complying with the abovementioned description 131 ex85421159 #31 FIFO (first in, first out) read/write memory of C-MOS technology, with a storage capacity of 72 Kbits and with an access time not exceeding 15 no or a programable flag register, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: CV7C460-15 IDT 7205 CV7C470 #IT 53C4K18D4-15 or - other identification markings relating to devices complying			or	• '
technology, for simultaneous reading and writing in one clock signal or bidirectional operation, with a storage capacity of 9 or 18 Kbits, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: CV7C439 CV7C453 IDT 72031 CV7C443 CV7C455 MT S3C51218A1 CV7C445 CV7C456 CV7C446 IDT 72021 or - other identification markings relating to devices complying with the abovementioned description it is a season of the season of the season of the following combinations of figures and letters: If I ex85421159 and the season of the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: CV7C468-15 IDT 7265 CV7C470 MT 53C4K18D4-15 or - other identification markings relating to devices complying				0
CV7C439 CV7C453 IDT 72031 CV7C443 CV7C455 NT 53C51218A1 CV7C445 CV7C456 CV7C446 IDT 72021 or - other identification markings retating to devices comptying with the abovementioned description ### 131 ex85421159 ### 157 ### 1585421159	130 ex85421159	•27	technology, for simultaneous reading and writing in one clock signal or bidirectional operation, with a storage capacity of 9 or 18 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of	
or - other identification markings relating to devices complying with the abovementioned description ### PIFO (first in, first out) read/write memory of C-MOS technology, with a storage capacity of 72 Kbits and with an access time not exceeding 15 ns or a programmable flag register, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: CV7C460-15			CV7C439 CV7C453 IDT 72031 CV7C443 CV7C455 NT 53C51218A1 CV7C445 CV7C456	
- other identification markings relating to devices complying with the abovementioned description 131 ex85421159 #31 FIFO (first in, first out) read/write memory of C-MOS technology, with a storage capacity of 72 Kbits and with an access time not exceeding 15 ns or a programmable flag register, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: CY7C468-15 IDT 7205 CY7C470 MT 53C4K18D4-15 or - other identification markings relating to devices complying				
FIFO (first in, first out) read/write memory of C-MOS technology, with a storage capacity of 72 Kbits and with an access time not exceeding 15 ns or a programmable flag register, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: CY7C468-15 IDT 7285 CY7C470 MT 53C4K18D4-15 or - other identification markings relating to devices complying			- other identification markings relating to devices complying	
technology, with a storage capacity of 72 Kbits and with an access time not exceeding 15 ns or a programmable flag register, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: CY7C460-15 IDT 7205 CY7C470 MT 53C4K18D4-15 or - other identification markings relating to devices complying		<u>'</u>		8
CV7C470 MT 53C4K18D4-15 or - other identification markings relating to devices complying	131 ex85421159	#31	technology, with a storage capacity of 72 Kbits and with an access time not exceeding 15 ns or a programmable flag register, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of	
- other identification markings relating to devices complying			CY7C460-15 IDT 7205	-
	T .		or .	
with the Booteman description			 other identification markings relating to devices complying with the abovementioned description 	6

	CN code		TARIC	Description	Rate of autonomous duty (%)
132	ex85421159		*71	FIFO (first in, first out) read/write memory of C-MOS technology, capable of asynchronous reading and writing, with a storage capacity of 512 x 9 bits, 1 K x 9 bits, 2 K x 9 bits or 4 K x 9 bits and an access time not exceeding 15 ns, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	•
	•			CY7C421 CY7C425 CY7C429 CY7C433	
				or .	
				 other identification markings relating to devices complying with the abovementioned description 	
133	ex85421162 ex85421164 ex85421167 ex85421168		*03 *03 *03 *01	Floating-point arithmetic co-processor, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures or figures and letters:	
				3171 80387 MC 68882 TX 32081 W 74 ACT 8847 8087 NCR 32020 US 83C87 79 R 2010 80C287 NS 32081 US 83S87 79 R 3010 CV 7C 602 NS 32381 WTL 3167 80287 MC 68881 R 2010/16	
				or	
		•		 other identification markings relating to devices complying with the abovementioned description 	θ
134	ex85421162		1 42	Microprocessor with a processing capacity not exceeding 8 bits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures or figures and tetters:	
		,		82389 CXD 2515Q MC68HC11EDO Z 64180 8294 CXD 2601 MC68HC11F Z 8068 82C389 HD 644180 PC 87323VF Z 86C95 Am 79C410 HD 648180 PC 87911 Z 9518 Am 79C411 MC 68362 SC414950FB	
				Or	
				 other identification markings relating to devices complying with the abovementioned description 	θ
136	ex85421164		#45	Microprocessor with a processing capacity exceeding 8 bits but not exceeding 16 bits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
	•			1TX9-0301 ADSP 2101 MC 68302 76001FCCHFA ADSP 21028S-50 PD 70236 76005 CN CF 70064 TMS 38010 80186 CF 70095 TMS 380C16	
	٠.	•		80188 CF 70200 TMS 380C24 80286 CF 72301 TMS 380C25 80C186 CF 72305A TMS 380C26	
-				80C188 CIFAX TMS 380C27 80C196 DSP 56116 XSP 56200 80C296 DSP 56156 Z 280 82389 DSP 56200 Z 70108 82C389 MB 89T713 Z 70116 ADSP 2100 MB 89T715	
				or .	
	4.			 other identification markings relating to devices complying with the abovementioned description 	- 0

	CN code	TARIC	Description	Rate of autonomous duty (%)
139	ex85421167	#41	Microprocessor with a processing capacity exceeding 16 bits but not exceeding 32 bits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and tetters:	
			398 Z 50 82C389 MC 68040 PC 604 486 86C020 MC 68331 R 2000/16 78201441 Am 29000 MC 68EC020 ST 18931 7982000A CPU 04041871 MC 88110 ST 18932 7983000 CV7C601 NCR 32000 ST 18941 80386 DC 262 A NS 32032 ST 188942 80486 DSP 32 C NS 32332 TMS 320M500 80501 L 64801 NS 32532 TMS 320M520 80501 L 64801 NS 32532 TMS 320M520 80508 MB 86904CR NS 32C032 TMX 320M440 82389 MC 68020 PC 601 W 8701	
		•	or	
			 other identification markings relating to devices complying with the abovementioned description 	0
140	ex85421168	*32	Microprocessor with a processing capacity exceeding 32 bits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures or of figures and letters:	
			21064 80860 R 4000 R 4300 R 4400	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	0
141	ex85421171	+02	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROH) with a storage capacity of 224, 256 or 320 Kbits, a random-access memory (RAM) with a storage capacity of 3,5 or 4,5 Kbits, an analogue-to-digital converter, a timer/counter, a display control circuit, a bus interface, a remote control circuit, an interrupt controller and a clock generator, in the form of a monotithic intergrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			CXP 85228 CXP 85232 <u>CXP 85340</u>	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	θ
142	ex85421171	*03	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 192 Kbits, random-access memories (RAMs) with a total storage capacity of 6400 bits, an anatogue-to-digital converter, a timer/counter, a programmble pattern generator, a servo input control circuit, a serial interface and a clock generator, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
-			CXD 80724	
		•	or - other identification markings relating to devices complying with the abovementioned description	- e

	CN code	TARIC	Description	Rate of autonomous duty (%)
144	ex85421171	#84	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 256 or 320 Kbits and a random-access memory (RAM) with a storage capacity of 18496 bits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			CXP 87132 CXP 87140	
			OF .	
		*	 other identification markings relating to devices complying with the abovementioned description 	0 .
146	ex85421171	•85	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising of a read only memory, non-programmable (ROM) with a storage capacity of 192 Kbits, a random-access memory (RAM) with a storage capacity of 4 Kbits, an analogue-to-digital converter and a fluorescent tube display drive circuit, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			TMP87CK70AF	
			07	
			- other identification markings relating to devices complying with the abovementioned description	0
148	ex85421171	#86	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 160 or 256 Kbits, a random-access memory (RAM) with a storage capacity of 5 or 8 Kbits, a random-access memory (RAM) with a storage capacity of 1280 bits, and a display control circuit, in the form of a monolithic intergrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			M37500M5 M37500M8	
			or .	•
			 other identification markings relating to devices complying with the abovementioned description 	θ
149	ex85421171	*12	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) or a programmable, non-erasable, read only memory (PROM) or a UV erasable, programmable, read only memory (EPROM) with a storage capacity of 64, 128 or 256 Kbits, a random-access memory (RAM) with a storage capacity of 4 or 8 Kbits and a display control circuit, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			N 38203E4 N 38203N2 N 38207E8 N 38207N8	
	•		or	•
			 other identification markings relating to devices complying with the abovementioned description 	θ
159	ex85421171	•13	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 96 Kbits or of 127 Kbits or more but not exceeding 384 Kbits, a random-access memory (RAM) with a storage capacity of 2 Kbits or more but not exceeding 8 Kbits, a serial interface circuit and a display control and drive circuit, in the form of a monolithic intergrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	-
			CXP 82316 PD 75316 TMP 87CH20F	
			in visited	\ .

,	CN code	TARIC	Description				Rate of a	utonomous duty	(x)
			MB 89098	TMP 870020F	7		•		
			or						
				fication mark vementioned o		to devices complying	θ.		
151	ex85421171	*04				V 1 k 1	".		
191	ex83421171	•24			•	S technology, with a egister-to-register			
						access memory (S-RAM) 12 Kbits and at least	2	•	
			read only memo	ry, non-progi	rammable (ROM)	or a programmable,	•		
						an UV-erasable, . · an electrically			
			erasable, prog	rammable, rea	ad only memory	(E ² PROM), with a its, in the form of a			
						in a housing bearing:			
	•				g consisting of ns of figures t	for including one of			
			•	- •	J			•	
			370C010 370C032	370C250 370C256	370C732 370C756	73C85 73C88			
			3700050	3700310	3700758	73095		•	•
			370C052 370C056	3,70C332 370C350	370C810 370C850	73C161 MC 68HC05P1	•	•	
	-		370C058 370C150	370C352 370C356	374C036 73C41	MC 68HC05P8			
			3700156	370C358	73042				
			or	•					
				fication mark		to devices complying	θ · ·		
	ex85421171	\$26				6 technology, with a	· · · · · · · · · · · · · · · · · · ·		· · · · · · · · · · · · · · · · · · ·
			non-programmab only memory (P memory (EPROM) not exceeding a storage capa monotithic int - an identific	te (ROM) or a ROM) or an U with a stor 256 Kbits and city not exce egrated circ ation markin	a programmable V erasable, pro age capacity of d a random-acco eeding 3 Kbits uit contained	g a read only memory, non-erasable, read or			
			83L51FC	87C5 <u>8</u>	MC68HC05i8			•	
		•	87C51 87C54	M50958 M50959	MC68HC705i8 MN 1871215			•	
								•	
			or			1.			
				fication mark vementioned		to devices complying	8		
153	ex85421171	*29	processing cap (RAM) with a s	acity of 8 b torage capac	its, comprising ity of 4 or 6 l	S technology, with a g a random-access memor (bits, a read only rammable, non-erasable,	-		
						e, programmable, read city of 96 or more but			
		•	not exceeding	192 Kbits, a	n electrically	erasable programmable,	•		
						capacity of 4 or 5 at converter, in the			
			form of a mono bearing:	lithic integ	rated circuit	contained in a housing			
	•		- an identific		g consisting o	f or including one of and letters:			
` -	•		MC 68HC11E9	MC 68HC	11L6 MC 6	BHC711K4FN			
			MC 68HC11K4	MC 68HC		BHC711K4FS		•	
			or						
		•	- other identi	fication mar	kings relating	to devices complying	-		
· 				vementioned			θ .		
									

	CN code	TARIC	Description	Rate of autonomous	duty (%)
154	ex85421171	#42	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, providing protocol control, data formatting and audio signal processing, comprising 1 or 2 random-access memories (RAMs) with a total storage capacity not		
		·	exceeding 10 Kbits and a read only memory, non-programmable (ROM) with a storage capacity of 192 Kbits, in the form of a monotithic intergrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:		
			Am 79C412		
			or		
			 other identification markings relating to devices complying with the abovementioned description 	8 .	
155	e×85421171	#43	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a 16-bit digital signal processor, a random-access memory (RAM) with a storage		
	•		capacity of 4 Kbits or more but not exceeding 16 Kbits and having the function of programme memory, 2 random-access		
	·		menories (RAMs) with a total storage capacity of 2 Kbits or more but not exceeding 8 Kbits and 256 registers, in the form of a monolithic intergrated circuit contained in a housing	•	
			bearing: - an identification marking consisting of or including one of		
			the following combinations of figures and letters:	•	
		•	<u>Z 86294 </u>	•	
			or		
			 other identification markings relating to devices complying with the abovementioned description 	0	
156	ex85421171	* #44 -	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, providing floppy disc storage unit or keyboard control functions, comprising an 8-bit configuration register, a random-access memory (RAM) with a		
			storage capacity of 16 Kbits and having the function of programme memory, a random-access memory (RAM) with a storage capacity of 2 Kbits and a real-time clock, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:		•
			PC 87323VF PC 87911		
			or		
			 other identification markings relating to devices complying with the abovementioned description 	0	
157	ex85421171	≇4 5	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, providing keyboard control functions, comprising a read only memory, non-programmable (ROM) with a storage capacity of 2 Kbits, random-access memories		
		•	(RAMs) with a total storage capacity of 2 Kbits, a real-time clock, address registers and input/output buffers, in the form of a monolithic integrated circuit contained in a housing bearing:		
			 an identification marking consisting of or including the following combination of figures and letters: 		
			82C113		
-		. "	or		
			 other identification markings relating to devices complying with the abovementioned description 	0	
158	ex85421171	. #46	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, providing vertical deflection functions for a cathode-ray tube, comprising 2 arithmetic-logic units (ALUs), 4 read only memories, non-programmable (ROMs) with	-	
			a total storage capacity of 11,7 Kbits, 2 random-access memories (RAMs) with a total storage capacity of 1 Kbit, an analogue-to-digital converter and 2 digital-to-analogue converters, in the form of a monolithic integrated circuit		

CN code	TARIC	Description	Rate of autonomous duty (%)	
		contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:		
		CXD 2018		
	,	or		
		- other identification markings relating to devices complying with the abovementioned description	. 0	
159 ex85421171	±47	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 48 or 64 Kbits, a random-access memory (RAM) with a storage capacity of 1 Kbit or 1536 bits and 6 digital-to-analogue converters, in the form of a monolithic integrated circuit contained in a housing bearing:	•	
		- an identification warking consisting of or including one of the following combinations of figures and letters:	• .	
		PCA 84C640 PCA 84C840 PCA 84C841		
		or		
		- other identification markings relating to devices complying with the abovementioned description	0	_
160 ex85421173	* 06	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, comprising a static		-
		random-access memory (S-RAM) with a storage capacity of 8 Mbits and having the function of programme memory, a static random-access memory (S-RAM) with a storage capacity of 8 Kbits, a system integration module (SIM), an anlogue-to-digital	· ·	
•		converter a timer, 2 serial interface circuits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:		
	·	MC 68HC16Z1 (SC415902FV)		
		or	•	
		 other identification markings relating to devices complying with the abovementioned description 	8	_
162 ex85421173	* 08	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, comprising a read only memory, non-programmable (ROM) or a programmable, non-erasable, read only memory (PROM) or a UV erasable, programmable, read only memory (EPROM) with a storage capacity of 128, 192, 256, 384 or 480 Kbits, a random-access memory (RAM) with a storage capacity of 4, 8 or 16 Kbits and an 8-bit analogue-to-digital		
		converter, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:		•
	•	M 37702 E2 M 37702 H2 M 37702 M4 M 37702 E4 M 37702 H8 M 37702 M6L M 37702 E8 M 37702 H3B M 37702 M0B		•
		or		
		 other identification markings relating to devices complying with the abovementioned description 	θ	
181 ex85421173	*10	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, for controlling dataflows between a twisted-pair-cable local area network (LAN) and a central processing unit (CPU), comprising a read only memory,		
	•	non-programmable (ROM) with a storage capacity <u>not exceeding 32</u> <u>Kbits</u> , a random-access memory (RAM) with a storage capacity <u>not exceeding 128 Kbits</u> , in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	-	
		SMC 83C825		
		or		
			4	Į
			2-	<u>ب</u>

CN code	TARIC	Description	Rate of autonomous duty ()
-		- other identification markings relating to devices complying with the abovementioned description	8
63 ex85421173	*11	Microcontroller or microcomputer of C-MOS technology, with a	
. •		processing capacity of 16 bits, comprising a multipliar/accumulator (MAC), an arithmetic-logic shifter, a static random-access memory (S-RAM) with a storage capacity of	•
		48 Kbits and having the function of programme memory, a static random-access memory (S-RAM) with a storage capacity of 16 Kbits	
		and a programmable timer, in the form of a monolithic integrated circuit contained in a housing bearing:	
		 an identification marking consisting of or including one of the following combinations of figures and letters: 	
		ADSP 2181 ADSP 21828S-58	
		or	• • •
		 other identification markings relating to devices complying with the abovementioned description 	
64 ex85421173	#12	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of <u>16</u> bits, comprising an 8-bit <u>or 16-bit</u>	
		external data-bus, a random-access memory (RAM) with a storage capacity of 16 Kbits and having the function of programme memory, a random-access memory (RAM) with a storage capacity of	
		16 Kbits, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the	
		following combination of figures and letters:	
		DSP 56116	•
		or .	
		 other identification markings relating to devices complying with the abovementioned description 	8
65 ex85421173	*13	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, providing local network bus	
		communication control, comprising a random-access memory (RAM) with a storage capacity of 2 Kbits, 2 read only memories, non-programmable (ROMs) or 2 programmable, non-erasable, read	
		only memories (PROMs) or 2 UV erasable, programmable, read only memories (EPROMs) with a total storage capacity of 56 or 128	
		Kbits and a serial-port interface circuit, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		TMS 8370C03 TMS 8370C73	
		or .	
		- other identification markings relating to devices complying with the abovementioned description	θ .
66 ex85421174	* 04	Microcontroller or microcomputer of C-MOS technology, with a	:
		processing capacity of 32 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 4 Mbits, a random-access memory (RAM) with a storage capacity of 1 Mbit, a	
		display control and drive circuit, an interrupt controller, a keyboard controller, a memory mapping control circuit and a	•
	. •	clock generator, in the form of a monolithic integrated circuit contained in a housing bearing:	•
		- an identification marking consisting of or including the	

- other identification markings relating to devices complying with the abovementioned description

θ

Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 19 bits, providing audio functions and transmit/receive functions of a digital cordless telecommunication system, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: AM 78C420 SC 14400 SC 14401 SC 14420 SC 14460 or - other identification markings relating to devices complying with the abovementioned description Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 32 bits, comprising 16-bit data-buses and a 16 x 16 bits multiplier with 32 bits results, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters: TMS 320M520 or - other identification markings relating to devices complying with the abovementioned description 171 ex85421175 *23 Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 40 bits or more, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification markings relating to devices complying with the abovementioned description 172 ex85421177 *23 Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 40 bits or more, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification markings relating to devices complying with the abovementioned description or - other identification markings relating to devices complying with the abovementioned description the formal bus, ocaprising a microprocessor interface circuit, a voice/data receiver and transmitter, in the form of a bidirectional bus, coaprising a microprocessor interface circuit, a voice/data receiver and transmitter, in the form of a bidirectional bus, ocaprising a microprocessor interface circuit.	
or - other identification markings relating to devices complying with the abovementioned description 167 ex85421174 *14 Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 32 bits, comprising 16-bit data-buses and a 16 x 16 bits multiplier with 32 bits results, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters: TMS 320M520 or - other identification markings relating to devices complying with the abovementioned description 171 ex85421175 *23 Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 48 bits or more, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: CS 4920 DSP 56001 DSP 96002 DSP 56000 DSP 56166 TMS 3200500 or - other identification markings relating to devices complying with the abovementioned description Message handler circuit based on gate arrays of C-MOS technology, providing multi-channel communication over a bidirectional bus, comprising a microprocessor interface	
- other identification markings relating to devices complying with the abovementioned description 167 ex85421174 *14 Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 32 bits, comprising 16-bit data-buses and a 16 x 16 bits multiplier with 32 bits results, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification markings consisting of or including the following combination of figures and letters: TMS 320M520 or - other identification markings relating to devices complying with the abovementioned description 171 ex85421175 *23 Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 48 bits or more, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: CS 4920 DSP 56000 OSP 56000 OF - other identification markings relating to devices complying with the abovementioned description 173 ex85421177 *803 Message handler circuit based on gate arrays of C-MOS technology, providing multi-channel communication over a bidirectional bus, comprising a microprocessor interface	
with the abovementioned description 167 ex85421174 *14 Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 32 bits, comprising 16-bit data-buses and a 16 x 16 bits multiplier with 32 bits results, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters: TMS 320M520 or - other identification markings relating to devices complying with the abovementioned description 171 ex85421175 *23 Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 40 bits or more, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: CS 4920 DSP 56001 DSP 96002 DSP 56000 DSP 56166 TMS 328C500 or - other identification markings relating to devices complying with the abovementioned description 173 ex85421177 *803 Message handler circuit based on gate arrays of C-MOS technology, providing multi-channel communication over a bidirectional bus, comprising a microprocessor interface	
processing capacity of 32 bits, comprising 18-bit data-buses and a 16 x 16 bits multiplier with 32 bits results, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters: TMS 320M520 or - other identification markings relating to devices complying with the abovementioned description 171 ex85421175 *23 Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 40 bits or more, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and tetters: CS 4920 DSP 56001 DSP 96002 DSP 56000 DSP 56166 TMS 3200500 or - other identification markings relating to devices complying with the abovementioned description 173 ex85421177 *03 Message handler circuit based on gate arrays of C-MOS technology, providing multi-channel communication over a bidirectional bus, comprising a microprocessor interface	8
- other identification markings relating to devices complying with the abovementioned description 171 ex85421175 *23 Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 40 bits or more, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and tetters: CS 4920 DSP 56000 DSP 56166 TMS 3200500 or - other identification markings relating to devices complying with the abovementioned description 173 ex85421177 ***803** ***Message handler circuit based on gate arrays of C-MOS technology, providing multi-channel communication over a bidirectional bus, comprising a microprocessor interface	
- other identification markings relating to devices complying with the abovementioned description 171 ex85421175 *23 Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 40 bits or more, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: CS 4920 DSP 56000 DSP 56166 TMS 3200500 or - other identification markings relating to devices complying with the abovementioned description 173 ex85421177 *03 Message handler circuit based on gate arrays of C-MOS technology, providing multi-channel communication over a bidirectional bus, comprising a microprocessor interface	•
with the abovementioned description 171 ex85421175 *23 Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 40 bits or more, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and tetters: CS 4920 DSP 56000 DSP 56166 TMS 320C500 or - other identification markings relating to devices complying with the abovementioned description 173 ex85421177 *03 Message handlar circuit based on gate arrays of C-MOS technology, providing multi-channel communication over a bidirectional bus, comprising a microprocessor interface	
processing capacity of 40 bits or more, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: - CS 4920 DSP 56000 DSP 96002 - DSP 56000 DSP 56166 TMS 3200500 or - other identification markings relating to devices complying with the abovementioned description 173 ex85421177 #03 Message handler circuit based on gate arrays of C-MOS technology, providing multi-channel communication over a bidirectional bus, comprising a microprocessor interface	θ .
OSP 56000 DSP 56166 TMS 3200500 or - other identification markings relating to devices complying with the abovementioned description 173 ex85421177 #03 Message handler circuit based on gate arrays of C-MOS technology, providing multi-channel communication over a bidirectional bus, comprising a microprocessor interface	
- other identification markings relating to devices complying with the abovementioned description 173 ex85421177	
with the abovementioned description 173 ex85421177	• •
technology, providing multi-channel communication over a bidirectional bus, comprising a microprocessor interface	0
monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
OHV 253	
or	
 other identification markings relating to devices complying with the abovementioned description 	е
172 ex85421177	
- an identification marking consisting of or including one of the following combinations of figures or of figures and letters:	•
0391343 6006Z1 6008	
or	
 other identification markings relating to devices complying with the abovementioned description 	0
174 ex85421179	

	CN code		TARIC	Description	Rate of autonomous duty (%)
	, •			or	•
				 other identification markings relating to devices complying with the abovementioned description 	0
175	ex85421179	-	*05	Interface and control circuit based on standard cells of C-MOS technology, capable of data organisation and transfer to an inkjet printhead, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
				. 1MK9-0201	
				or	
			•	 other identification markings relating to devices complying with the abovementioned description 	0
176	ex85421179	•	≉ 86	Interface and control circuit based on standard cells of C-MOS technology, capable of interfacing between a disc storage unit, a central processing unit (CPU), a dynamic random-access read/write memory (D-RAM) and a local bus, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
	• .			6869968 6861902	•
				or	
				 other identification markings relating to devices complying with the abovementioned description 	θ
180	ex85421182		*82 ·	Printer control circuit of C-MOS technology, comprising a dynamic random-access memory (D-RAM), direct memory access (DMA) registers, registers for communication between 2 microprocessors and a static random-access memory (S-RAM), in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
				TMX 35C438	
				or	
				 other identification markings relating to devices complying with the abovementioned description, 	9
182	ex85421182		* 03	Bus and memory control circuit of C-MOS technology, providing bus arbitration and capable of controlling data transfer between a local bus, a central processing unit (CPU) and memory, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of	
		•		the following combinations of figures or figures and letters:	
			•	2782654 69G1785	•
	•	٠.		or	•
				 other identification markings relating to devices complying with the abovementioned description 	6
184bis	ex85421182		*05	Display controller or character generator for liquid crystal displays (LCDs), in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	•
-				HD 61830 LH 5821 MC 141540	· · · · · · · · · · · · · · · · · · ·
,	,		•	or	-
. .				 other identification markings retating to devices complying with the abovementioned description 	0

CN code	TARIC	Description	•			.' Rate of autonomous duty (%)
183 ex85421182	+06	integrated circu - an identificat	it contained ion marking o	in a housing b consisting of c	form of a monolithic learing: or including one of figures and letters:	
		82369 82355 82358 82374EB 82434LX 82 C 101 82 C 103	82 C 288 82 C 301 82 C 320 82 C 362 82 C 461 82 C 463 82 C 465 82 C 493 82 C 496	82 C 591 82 C 88 CA 91C014 ET 6000 GC 181 HT 216 L1A 4661 MSM 6307 R 4220	R 4230 TACT 83443 VAC 868 VIC 868 VIC 64 VL 82 C 331 VV 86 C 410	
		or `				•
·		 other identifi with the above 			devices complying	0
184 ex85421182	*10	- a) cathode-ray	tube control tal display (ntrolling,	.ling, (LCD) driving o	following functions:	
		in a housing or	fixed on a plion marking o	astic support, consisting of c	or including one of	
		a)82 C 434 a)82 C 453 a)82 C 801 a)86 C 805 a)86 C 911 a)86 C 928 a)AM 8052 a)ATI 68800 a)CL-GD542 a)CLT 9087 a)CRT 907	b)H 5050 b)HD 44100 b)HD 44780 b)HD 66100 b)HD 611051 b)HD 661061 b)HD 661071 b)LC 7582 b)H 6003 b)H 6004 b)MSH 5259	b)V 6118 b)V 6120	212 213 -0J 24 31	
		a)ET 4000 a)M 50452 a)MB 89321 a)MB 89322 a)TVGA 8900C a)V 6363 a)VV 86 C 310 a)WD 90 C 10 a)WD 90 C 11 a)WD 90 C 30 a)WD 90 C 31 a)WD 90 C 33 a)WD 90 C 33 a)WD 90 C 33	b) MSH 5298 b) MSH 5299 b) MSH 5839 b) PCF 8576 b) SED 1520 b) SED 1610 b) SED 1610 b) T 6439 b) T 6440 b) TMS 3491 b) TMS 3492 b) TMS 57202	C)82 C 45 C)82 C 45 C)84 C 45 C)84 C 45 C)AVGA1 C)CL-GD54 C)HT 209 C)L 64845 C)NCR 777 C)OTI 067 C)PEGA C)PVGA C)WD 90 (51 52 51 110 5 5 5 222	
		b)CL-GD6410 b)COP 472	b) THS 57206 b) THS 57207	_	53	
		or				
	·	- other identifi with the above			o devices complying	θ
185 ex85421182	*36	direct memory ac controllers, in contained in a h	cess (DMA) co the form of a ousing bearing ion marking o	ontrollers and nonolithic in ng: consisting of c	ntegrated circuit or including one of	•
· 		82360SL 82C206 82C316	82C491 82C593 6C 101 SX	HT 101 SX VL 82 C 480 VL 82 C 486		
		or - other identifi with the above			o devices complying	0

	CN code	TARIC	Description	Rate of autonomous duty (%)
186	ex85421182	*39	DC motor control circuit, with at teast one of the following caracteristics: - a) for a brushless three-phase motor, of BiMOS technology, operating at a single power supply of 5 V, comprising outputs with an impedance of 1,4 ohms at a power of 1 A, - b) of C-MOS technology, comprising a circuit to monitor power supply, a circuit to store and decode addresses and to multiplex data, an 8-bit digital-to-analogue converter and 5	
			amplifiers, - c) of N-MOS (including H-MOS) technology, comprising a digital 16-bit filter,	
			in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			a)9990CS b)GC 27 b)GC 45 c)LM 629	
			or	•
		•	 other identification markings relating to devices complying with the abovementioned description 	. 0
187	ex85421182	•89	Line decoder/driver of C-MOS technology, with an output voltage of 30, 35 oder 60 V at 500 mA, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of	
			the following combinations of figures and letters: MC 34142 UCN 5816 UCN 5817	
	•		or	
			- other identification markings relating to devices complying with the abovementioned description	θ
188	ex85421182	192	Control circuit of C-MOS technology, capable of driving 25 tamps or a 7-segment light-emitting diode (LED) display, having a drive voltage of 4,5 V or more but not exceeding 6 V, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the	
			following combination of figures and letters: MC 14489	
	•.		or	
			 other identification markings relating to devices complying with the abovementioned description 	θ
189	ex85421182	9 93	Control circuit of C-MOS technology, for the firing of printhead pens, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the	
	,	**	following combination of figures and letters : 1TV5-0001	•
			or	
			 other identification markings relating to devices complying with the abovementioned description 	9
190	ex85421182	•94	Control circuit of C-MOS technology, capable of managing the reduction of power consumption of a microprocessor or of other peripheral units, in the form of a monolithic integrated circuit contained in a housing bearing: I an identification marking consisting of or including the following combination of figures and letters:	
		•	1928 CP	-
· .			or	
			 other identification markings relating to devices complying with the abovementioned description 	8

	CN code	TARIC	Description	Rate of autonomous duty (%)
298	ex85421184	* 02	Pulse-code-modulation line interface circuit of C-MOS technology, comprising a line transmitter, a line receiver, an encoder/decoder, a jitter attenuator, a clock and data recovery circuit, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			CS 61574 CS 61575	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	θ
193	ex85421184	* #14	Interface or interface and control circuit of C-MOS technology, with at least one of the following functions: - a) for signals between a peripheral disc memory unit and a central processing unit (CPU), - b) for controlling data communication between a system bus interface and peripheral units, comprising a system interface	
			gate, a microprocessor gate and a direct memory access (DMA) gate, - c) for interfacing and controlling the data sequence between an automatic data-processing machine and a disc storage unit, - d) for read/write data between a digital-audio-tape storage unit and a microprocessor,	
			in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			a)82C611	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	θ
194	ex85421184	\$ 46	Control and interface circuit of C-MOS technology, comprising 48 mA drivers, registers, an 18- or 32-bit direct memory access (DMA) interface, an 8- or 32-bit microprocessor bus and a parity generator and checker, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			AM53C974 NCR 53C94 NCR 53C95 NCR 53C96	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	8
204	ex85421186	*03	Video-tine comb filter of C-MOS technology, capable of digital signal tuminance/chrominance separation, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		• .	CXD 2024	•
			or	-
`. .			 other identification markings relating to devices complying with the abovementioned description 	θ

	CN code	TARIC	Description	Rate of autonomous duty (%)
205	ex85421186	≉ 84	Digital-to-analogue and analogue-to-digital converter of C-MOS technology, comprising an analogue modulator capable of oversampling signals at a frequency of 1024 MHz and a filter capable of sampling signals from a digital modulator at a frequency of 512 kHz, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	•
			MSP 58C28	× .
			or .	
			 other identification markings relating to devices complying with the abovementioned description 	0
206	ex85421186	*67	Multiplexer/demultiplexer of C-MOS technology, capable of converting 28 independent signals, comprising a microprocessor bus interface circuit and a random-access memory (RAM) with a storage capacity of 256 bits, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			TXC 03301	•
			.	•
			 other identification markings relating to devices complying with the abovementioned description 	0
207	ex85421186	* 68	Subscriber line audio-processing circuit (SLAC) of C-MOS technology, comprising 2 digital signal processors, at least 1 analogue-to-digital converter and at least 1 digital-to-analogue converter, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	•
		•	Am 7981 Am 7985 Am 79082 Am 79083 Am 79084	
			or	
			- other identification markings relating to devices complying with the abovementioned description	7
268	ex85421186	*69	8-, 9-, 10-, 16- or 18-bit register of C-MOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
	•		74FCT162374 74FCT16823BT 74FCT574 74FCT162823BT 74FCT16823CT Am 29C818A 74FCT162823CT 74FCT374 Am 29C821A 74FCT16374 74FCT534 Am 29C823A	
			or	
	•	•	 other identification markings relating to devices complying with the abovementioned description 	9
209	ex85421186	*10	Transmitter of C-MOS technology, operating with a single power supply of +5 V and with a data transfer rate per second of 120 Kbits or more at an output voltage of 5 V, comprising not more than 5 transmitters, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			MAX 200 MAX 204	•
			or .	
· .			 other identification markings relating to devices complying with the abovementioned description 	0

CN code	TARIC	Description	Rate	of autonomous	duty (x)
161 ex85421186	•11 •	Audio_signal processing_circuit of C-MOS_technology, comprising a read only memory, non-programmable (ROM), a random-access memory (RAM), 4 analogue-to-digital converters, a serial interface, a frequency decimation circuit and a loudspeaker overload protection circuit, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:		,	
		VV 27051			
		or	• .		
		- other identification markings relating to devices complying with the abovementioned description		· ·	
210 ex85421186	*12	Encoder/decoder of BiMOS technology, providing data conversion and separation and a data transfer rate of 50 Mbits per second, comprising a read pulse detector and a frequency			,
		synthesiser/synchroniser, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and latters:			•
	•	HD 153031 RF			
•		or			
		- other identification merkings relating to devices complying			
· .		with the abovementioned description		· · · · · · · · · · · · · · · · · · ·	
211 ex85421186	*16	Phase-locked loop (PLL) clock circuit of C-MOS technology, capable of synchronisation or multiplication of frequencies not exceeding 160 MHz, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and tetters:	•		
		MC 88915 MC 88916 MC 88920 MC 88FL117			
		or			*
		 other identification markings relating to devices complying with the abovementioned description 	0	•	
212 ex85421186	•21	Signal processing circuit of C-MOS technology, providing delay of scanning periods for horizontal image tines of a charge-coupled (CCD) image sensor, comprising a clockgenerator, a clamp circuit and a sample and hold circuit, in the form of a			
		 monotithic integrated circuit contained in a housing bearing: an identification marking consisting of or including one of the following combinations of figures and tetters: 			
		CXL 1517 MN 3860SA MSH 5819HS-K CXL 5504 MN 3861SA MSH 5834MS-X			
		- or.			
	•	 other identification markings relating to devices complying with the abovementioned description 	ė	. •	
196 ex85421186	•23	Circuit of BiMOS technology, for the recording and reproduction of data, operating at a <u>rate</u> not exceeding 12 Mbits/sec, comprising an encoding circuit, a decoding circuit, an analogue-to-digital converter, a digital equaliser filter and a random-access memory (RAM), in the form of a monotithic			
		integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:			
•		64G0166 (8189294)			
		•	-		
		or			

	CN code	TARIC	Description	Rate of autonomous duty (%)
199	ex85421186	#27	8-bit digital-to-analogue converter of C-MOS technology, capable of converting serial data input towards 36 output channels, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			MB 88344B	
			or	
			- other identification markings relating to devices complying · with the abovementioned description	0
213	ex85421186	#29	Transmitter/receiver of C-MOS technology, with at least one of the following characteristics:	
			- a) capable of connecting (terminating) line rates of .8448 or 34368 Kbits per second,	
		•	- b) of the 8-, 16- or 32-bit bidirectional type,	
			 c) operating with a single power supply of +5 V and a supply current not exceeding 2-mA, comprising 2 transmitters and 2 	
			receivers, - d) operating with a single power supply of +5 V, comprising at	
			least 6 transmitters and at least 8 receivers, - e) operating with a single power supply of +5 V and of +9 V or	
			more but not exceeding +13,2 V and with a data transfer rate per second of 120 Kbits or more at an output voltage of 5 V,	
			comprising 2 transmitters and 2 receivers, - f) having a supply current not exceeding 3 mA, comprising 2	
10			driver circuits and 3 receivers,	
			- g) with a propagation delay not exceeding 11 ns, - h) for signals between an encoder/decoder using Manchester	•
		,	code (MED) or an interface unit and a twisted pair cable or a coaxial cable in a local area network (LAN),	
			in the form of a monolithic integrated circuit contained in a	
			housing bearing: - an identification marking consisting of or including one of the following combinations of figures or of figures and letters:	
			a)TXC 02050 b)74 FCT 162652 d)MAX 246	•
	•		b)29 C 833 A b)74 FCT 162952 d)MAX 247	•
			b)29 C 853 A b)74 FCT 16500 d)MAX 248 b)74 AC 16472 b)74 FCT 16501 d)MAX 249	
		•	b)74 AC 16646 b)74 FCT 16543 e)MAX 201	
		,	b)74 AC 16657 b)74 FCT 16646 f)LTC 902 b)74 ACT 16245 b)74 FCT 16652 g)29 FCT 52	•
			b)74 ACT 16478 b)74 FCT 16952 g)82503	
			b)74 ACT 16863 b)74 FCT 245 b)83C92	
·			b)74 FCT 162500 b)74 FCT 645 h)83C94 b)74 FCT 162501 c)MAX 220 h)Am 79C98	
			b)74 FCT 162543 d)MAX 244 h)MC 145572	
			b)74 FCT 162646 d)MAX 245 h)TMS 380C60	
			or	
	•	•		•
			 other identification markings relating to devices complying with the abovementioned description 	0
200	ex85421186	*36	Encoder/decoder of C-MOS technology, capable of encoding,	•
		*	decoding and interfacing serial signals having a rate of 13 Kbits per second and audio signals having a rate of 104 Kbits	
			per second, comprising an analogue-to-digital converter, a digital-to-analogue converter, digital-pulse-code-modulation	· •
		•	filters and an echo cancellation circuit, in the form of a	
_		•	monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the	. •
			following combination of figures and Letters:	
			VP 22020	
		•	or	-
- •			- other identification markings relating to devices complying with the abovementioned description	0
			with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
201 ex85421186	*37	Delineation circuit of C-MOS technology, capable of extracting and inserting asynchronous transfer mode (ATM) cells from and into a line interface signal, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		TXC 05150	
i		 other identification markings relating to devices complying with the abovementioned description 	0
214 ex85421186	*39	Clock generator, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures or figures and letters:	
		32 D4661CL CY7B992 ICS 90C64 PCLK 1 82 C 402 DP 8531 LZ 93F31 PCLK 2 AV 9129 DP 8532 LZ 93F33 SC 11410 Bt 438 DP 83241 LZ 93K61 SC 11411 Bt 439 ICD 2023 MK 1418 SC 11412 CXD 1035 ICD 2027 MK 1442 TCK 9002 CXD 1252 ICD 2028 MK 1448 WD 90 C 61 CXD 1255 ICS 1394 MK 1450 CY7B991 ICS 2494 MSM 5547	
		or .	
		 other identification markings relating to devices complying with the abovementioned description 	θ
215 ex85421186	*40	Data-buffer or data/address-buffer circuit, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		82C592 GC 102 VL 82 C 332 FB 2020 HT 102	
· · · · · · · · · · · · · · · · · · ·		 other identification markings relating to devices complying with the abovementioned description 	0
216 ex85421186	#44	Data or image compression/decompression circuit of C-MOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		1XH4-0301 1XV9-0001 CL 450 CL 950 1XK6-0301 74 ACT 6340 CL 550	
	•	or .	
· · · · · · · · · · · · · · · · · · ·		 other identification markings relating to devices complying with the abovementioned description 	9
203 ex85421186	#45	Adaptive differentiated pulse-code-modulation encoder/decoder of C-MOS technology, comprising a pulse-code-modulation encoder/decoder interface circuit, a transmit and receive control circuit, a microprocessor bus interface circuit and a parallet port, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	·
		VP 86565	
	-	or	
- 		 other identification markings relating to devices complying with the abovementioned description 	0 .

CN code	TARIC	Description	Rate of autonomous duty (%)
217 ex85421186	■53	Signal generator of C-MOS technology, providing synchronous pulse generation for a charged coupled (CCD) image sensor, in the form of a monolithic integrated circuit contained in a	
		housing bearing: - an identification marking consisting of or including one of	
		the following combinations of figures and letters:	
*		CXD 1838 CXD 1217 LZ 93853 LZ 93N43 LZ 95652	
		or	
		 other identification markings relating to devices complying with the abovementioned description 	
218 ex85421186	•54	Modulator/demodulator of C-MOS technology (C-MOS-Modem), only for half duplex transfer of image telegraphy (facsimile) at a rate of 300, 2400, 4800, 7200 or 9600 bits per second, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the	
•		following combination of figures and letters:	
*		TC 35128	
		or	
		 other identification markings relating to devices complying with the abovementioned description 	0 -
219 ex85421186	•59	Signal processing circuit of C-MOS technology, capable of processing video-signals from a charge-coupled (CCD) image sensor, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of	,
		the following combinations of figures and letters:	•
		CXA 1810 CXD 2100 CXD 2150	
		or .	
•		 other identification markings relating to devices complying with the abovementioned description 	0
222ə ex85421188	*61	Mafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of clock and data recovery circuits, for use in the manufacture of goods of subheading 85421198 contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		GD 16042 GD 16043	
		or	
		- other identification markings relating to devices complying with the abovementioned description (a)	0
222b ex85421188	*82	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of multiplexer circuits, capable of multiplexing 4 data flows into a single data flow, comprising a phase-locked toop (PLL) circuit and laser diode drivers, for use in the manufacture of goods of subheading 85421198 contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
•		GD 16054	
-		or	
		 other identification markings relating to devices complying with the abovementioned description (a) 	0
_222d ex85421188	*03	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of transmitter/receivers, providing serial data communication at a rate of 622 Mbits per second, for use in the manufacture of goods of subheading 85421198 contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	

				•
	CN code	TARIC	Description	Rate of autonomous duty (%)
			GD 16964	
			OF .	
			- other identification markings relating to devices complying	
			with the abovementioned description (a)	θ
222g	ex85421188	104	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of multiplexers or demultiplexers, providing differential ECL level data input/output at a rate of 622 Mbits per second and TTL input/output signals at a rate of 78 Mbits per second, for use in the manufacture of goods of subheading 85421195 contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and tetters:	
		•	GD 16131 GD 16132	
			OF .	
	·		 other identification markings relating to devices complying with the abovementioned description (a) 	8
222í	ex85421188	+05	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of dual buffers for ECL/TTL level signals, for use in the manufacture of goods of subheading 85421198 contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
	-		GD 19225	•
	-	•	01	
		•	 other identification markings relating to devices complying with the abovementioned description (a) 	9
223	ex85421189	•18	Transmitter/receiver of gattium arsenide (GaAs) semiconductor material, providing synchronous/asynchronous data communication at a rate per second of 622 Mbjts or more but not exceeding 2,5 Gbits, in the form of a monotithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 85421198 contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and tetters:	
			GIGA BOA GIGA MATCH	
	- 1		or	
	•		 other identification markings relating to devices complying with the abovementioned description (a) 	. 6
224	ex85421189	*29	Measurement circuit of gattium arsenide (GaAs) semiconductor materiat, capable of measuring signat propagation times on transmission times, comprising 2 asynchronous counters, 4 comparators, a ctock generator and an oscillator, in the form of a monotithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 85421198 contained in a housing bearing: - an identification marking consisting of or including the	
			following combination of figures and letters:	
		•	or	
-			 other identification markings relating to devices complying with the abovementioned description (a) 	0
225	ex85421189	#38	Clock and data recovery circuit of gallium arsenide (GaAs) semiconductor material, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 85421198 contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	<u>-</u>

	CN code	TARIC	Description				Rate of aut	onomous duty (%)
			or Or			•		
			- other identificat with the abovemen		. • .	ces complying	0	
226	ex85421189	•46	Multiplexer of gall capable of multiple comprising a phase-	xing 4 data flows	into a singl	le data flow,	•	
			drivers, in the for contained in a hous goods of subheading an identification	ing (chip), for u 85421198 contair	use in the mar ned in a housi	nufacture of ing bearing:		_
			following combina	tion of figures a	end letters:		•	
			GD 16854			•		•
			- other identificat	ion markings rela	atina to devic	ces compluing		
			with the abovemen		•		0	
227	ex85421189	* 58	Divider/detector ci semiconductor mater the range of 50 MHz frequency divider a a monotithic integr (chip), for use in	ial, capable of s to 1700 MHz, com nd a phase/freque ated circuit not	synthesizing (aprising a pre ency detector, contained in	frequencies in escaler, a , in the form of a housing		
		•	85421198 contained - an identification following combina	marking consisti	ing of or incl	luding the	•	
			GIGA FSS				•	•
	٠.		or					
			 other identificat with the abovemen 		. - .	ces complying	θ	
228	ex85421198	*21	Static random-acces S-RAM), with a stor form of a monotithi bearing:	age capacity not	exceeding 1	Kbit, in the		
			- an identification the following com letters:	· · . · ·	•			
			82 \$ 89 93422	93425	MBM 93419			•
			or				•	
	•		- other identificat with the abovemen		-	ces complying	0	•
229	ex85421190	•74	Programmable, non-e technology, with a the form of a monol housing bearing: - an identification the following com	storage capacity ithic integrated marking consisti	not exceeding circuit containing of or incl	g 64 Kbits, in a sined in a sined in a sined in a sined in a sine of		
	•		18 S 030 27 S		6336	82 \$ 130		•
			24 S 10 27 S 24 S 41 28 L 24 S 81 28 L 28 L 42 28 L2 28 S 166 29613	22 53 8 241 22 54 8 570 XMFC 54 8 571	63 S 240 63 S 241 63 S 3281 7053 7058	82 \$ 131 93436 93446 Am 27843 MB 7115	•	
			28 \$ 42 29770 28 \$ 86 29771 27 PS 191 38510 27 PS 291 5305	5624 6305 6306 6308	74 S 570 74 S 571 76 LS 83 7620 7621	MB 7116 MB 7117 MB 7118 MB 7141		
		٠	27 \$ 12 5306 27 \$ 13 5308	6309 6335	92 S 114	MB 7142		•
<u>.</u>			or '					• .
			- other identificat with the abovemen			ces complying	. 0	•
					···		· -	

CN code	TARIC	Description	Rate of autonomous	s dutu (X	.)
233 ex85421195	•01		kate of autonomous		
233	•61	Control circuit of bipolar technology, capable of driving laser diodes or other light-emitting diodes (LEDs), in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:			
	•	IDA 07318			
		or			
		 other identification markings retating to devices complying with the abovementioned description 	8		
234 ex85421195	∗ 62	Driver circuit of gallium arsenide (GaAs) semiconductor material, for controlling laser diodes or other light-emitting diodes (LEDs), in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of			
	•	the following combinations of figures and letters:			
		0r	•		
· ·				••	1
		 other identification markings relating to devices complying with the abovementioned description 	, θ,		
235 ex85421198	•01	Transmitter of bipolar technology, providing encoding/conversion of parallet data/commands into serial format, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of			•
	•'	the following combinations of figures and letters:	•		•
	•	AN 79168 AN 7968 AN 79865			
		or			
		 other identification markings relating to devices complying with the abovementioned description 	0		
239 ex85421198	• 02	Audio digital-to-analogue converter of bipolar technology, with a dynamic range of 96 dB or more, comprising an internal voltage reference, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:			
		PCM 63P			
		or			
		- other identification markings relating to devices complying with the abovementioned description	8		
236 ex85421198	+ 0\$	Receiver of bipolar technology, providing decoding/conversion of serial data/commands into parallel format, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:		٠,	
	. •	AM 79169 AM 7969 AM 79866	•		
	•	or	•		
		 other identification markings relating to devices complying with the abovementioned description 	8		
237 ex85421198	∌ 06	Transmitter or receiver of bipolar technology, capable of serial data communication at a rate of 110 Mbits or more but not exceeding 1,4 Gbits per second, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:			
		HDMP 1882 HDMP 1884			
		or			
		- other identification markings relating to devices complying			11

CN code	TARIC	Description	Rate of autonomous duty (%)
		with the abovementioned description	. θ
241 ex85421198	•27	16-bit digital-to-analogue converter of bipolar technology, comprising an internal voltage reference, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		DAC 712	
		or	
		- other identification markings relating to devices complying with the abovementioned description	8
242 ex85421198	#28	Prescaler of bipolar technology, having an input frequency not exceeding 1,5 GHz and a selectable 64/65 or 128/129 divide ratio, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		MC 12822 SC 12822	
		or	
		- other identification markings relating to devices complying with the abovementioned description	6
243 ex85421198	*32	Transmitter/receiver of bipolar technology, capable of converting data into serial or parallel format and bidirectional serial data transfer at a rate not exceeding 200 megabytes per second, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		MC 1005X1451	
,	•	or	•
		 other identification markings relating to devices complying with the abovementioned description 	θ
244 ex85421198	+33	32 x 32-bit differential crosspoint switch of bipolar technology, capable of switching at a data rate per second of 800 Mbits, in the form of a monotithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		\$ 2024	•
		or	
		 other identification markings relating to devices complying with the abovementioned description 	
245 ex85421198	*35	Transmitter/receiver of bipolar technology, capable of data transmission over a twisted-pair cable, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and latters:	
· ·		DP 83228	
		or	•
		- other identification markings retating to devices complying with the abovementioned description	8
246 ex85421198	#36	Clock generator/buffer of gallium arsenide (GaAs) semiconductor material, capable of frequency synchronisation or multiplication, in the form of a monolithic integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	_
,		GA 1000 GA 1086 GA 1088 GA 1110 GA 1085 GA 1087 GA 1089 GA 1210	

	TAPIC	Description	Rate of autonomous duty (%)
•		or	•
-	~	 other identification markings relating to devices complying with the abovementioned description 	0
222c ex85421910	*10	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of transimpedance amplifiers, operating at a bandwidth of 900 MHz, having a resistance not exceeding 4 kOhm, for use in the manufacture of goods of subheading 85421930 contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		GD 16085	
		or	
•		 other identification markings relating to devices complying with the abovementioned description (a) 	θ .
222e ex85421910	*28	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of laser diode control circuits, providing an output current in a range of 10 mA to 70 mA at a power supply of -5 V (±1 x), for use in the manufacture of goods of subheading 85421970 contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
•		GD 16077	
		or	
		 other identification markings relating to devices complying with the abovementioned description (a) 	. 0
222f ex85421910	*30	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of amplifiers with a typical output power of 25 dBm in a frequency range of 1850 MHz to 1950 MHz, comprising radiofrequency (RF) switches, for use in the manufacture of goods of subheading 85421930 contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
•		GD 12033	
		. or	
		 other identification markings relating to devices complying with the abovementioned description (a) 	0
222h ex85421910	*40	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of dual amplifiers with a typical gain of 18 dB at a frequency of 1,5 GHz, for use in the manufacture of goods of subheading 85421930 contained in a housing bearing: - an identification marking consisting of or including the	
	,	following combination of figures and letters:	,
,	•	GD 10012	
		or - other identification markings relating to devices complying	. 6
250 ex85421920	+38	with the abovementioned description (a) Control circuit of C-MOS technology, capable of driving inductive and resistive loads, having 4 outputs with a current of 2 A or more but not exceeding 7,2 A, in the form of a monotithic integrated analogue circuit not contained in a housing (chip), for the manufacture of motor control systems (a)	.0
<u>2</u> 51 ex85421920	*40	Transimpedance amplifier of gallium arsenide (GaAs) semiconductor material, operating at a bandwidth not exceeding 2,7 GHz, in the form of an monolithic integrated anatogue circuit not contained in a housing (chip), for use in the manufacture of products falling within subheading 85271190	

	CN code	TARIC	Description	Rate of autonomous duty (X)
247	ex85421920	*50	Differential amplifier of bipolar technology, with a gain not exceeding 375 and a nominal input voltage of 1 mVpp, in the form of a monotithic integrated analogue circuit not contained in a housing (chip), for use in the manufacture of products falling within heading No 8471 (a)	Ò
248	ex85421920	*60	Amplifier with an input current not exceeding 80 nA, in the form of a monolithic integrated analogue circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 85421930 contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			INA 101 OPA 111 OPA 121 OPA 2111	
	•		or - other identification markings relating to devices complying with the abovementioned description (a)	θ .
249	ex85421920	₽ 70	Amplifier with a programmable gain factor, in the form of a monolithic integrated analogue circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 85422858 contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			3696G	
	•		or	
,			 other identification markings relating to devices complying - with the abovementioned description (a) 	. 6
253	ex85421930	* 01	Intermediate frequency (IF) amplifier of bipolar technology, comprising a mixer, a receive signal strength indicator (RSSI), a detector, an oscillator and a multiplier, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			CXA 1744R	
	•		or	•
			 other identification markings relating to devices complying with the abovementioned description 	θ
256	ex85421930	*02	Microwave amplifier of bipolar technology, with a nominal gain of 18 dB at 0,5 GHz or 22,5 dB at 1 GHz and 32,5 dB at 0,1 GHz or 26 at 1,5 GHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of	
			the following combinations of figures and letters:	
			A-86 A-88 N18	
	•		 other identification markings relating to devices complying with the abovementioned description 	0
257	ex85421930	•03	Quadrupte amplifier of C-MOS technology, with an input current not exceeding 20 pA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			* LNC 668	
			or	
			- other identification markings relating to devices complying	

CN code	TARIC	Description	Rate of autonomous duty (X)
258 ex85421930	* 05	Amplifier of bipolar technology, with a typical supply current not exceeding 1 mA at a voltage of 12 V and a temperature of 25°C, in the form of a monolithic integrated analogue circuit contained in a housing bearing:	
		 an identification marking consisting of or including the following combination of figures and tetters: 	
		LM 1964	
		or .	
		 other identification markings relating to devices complying with the abovementioned description 	0
259 ex85421930	* 06	Amplifier of bipolar technology, with a typical operating frequency of 1,3 GHz, 2,3 GHz or 3 GHz and a single supply voltage of 5 V, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of	
		the following combinations of figures and letters:	
		CID CIE CIF CIG CIH CIJ	
		or	
		 other identification markings relating to devices complying with the abovementioned description 	0
260 ex85421930	• 07	Amplifier with an off-set voltage not exceeding 1000 µV at 25°C, in the form of a monolithic integrated analogue circuit contained in a housing bearing:	
		 an identification marking consisting of or including one of the following combinations of figures and letters: 	
		LT 1006 OPA 275 OPA 628	
•		or	
		 other identification markings relating to devices complying with the abovementioned description 	. 0
261 ex85421930	* 68	Amplifier of gallium arsenide (GaAs) semiconductor material, having a nominal gain of 18 or more but not exceeding 30 dB and a frequency range of not more than 1,9 GHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing:	
		- an identification marking consisting of or including one of the following combinations of figures and letters:	
		166071 166072 166074 MGF 7131	
		07	
		 other identification markings relating to devices complying with the abovementioned description 	0
254 ex85421930	#09	Audio amplifier of bipolar lechnology, with a typical gain of 47 dB <u>in</u> a frequency <u>range</u> of <u>20 Hz to 20</u> kHz, in the form of a monolithic integrated analogue circuit contained in a	
	·	housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		TA 201S	
	*	or	•
		 other identification markings relating to devices complying with the abovementioned description 	6
262 ex85421930	±10	Amplifier, in the form of a monolithic integrated analogue	
		circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	-
		C 05 V 35	
		or ·	
		- other identification markings relating to devices complying	

	CN code	TARIC	Description	Rate of autonomous duty (%)
			with the abovementioned description These devices are for use in the manufacture of products falling within subheading 90214000 (a)	0
255	ex85421930	*11	Transimpedance amplifier of bipolar technology, with a typical gain of 72,5 dB at a frequency of 75B MHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the	•
			following combination of figures and tetters:	,
			ITA 12318	
		-	 other identification markings relating to devices complying with the abovementioned description 	
263	ex85421930	*15	Amplifier with an input current not exceeding 80 nA, in the form of a monolithic integrated analogue circuit contained in a housing bearing:	
			- an identification marking consisting of or including one of the following combinations of figures and letters:	
	•		<u>INA 181</u> OPA <u>2</u> 7 OPA 37 OPA 111 OPA 121	•
			or	
			 other identification markings relating to devices complying with the abovementioned description 	0
264	ex85421930	₽25	Variable amplifiers for the range of frequencies of 10 Hz or more but not exceeding 30 kHz, with a gain of 85 dB or more, in the form of a monolithic integrated analogue circuit contained in a housing bearing:	•
			 an identification marking consisting of or including the following combination of figures and letters: 	
			M 5218	
			or .	
			- other identification markings relating to devices complying with the abovementioned description	0
265	ex85421930	*30	Dual or quadruple amplifier operating with a supply current per amplifier not exceeding 400 µA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
	•		914B LT 1079 LT 1179 MC 14574 LT 1078 LT 1178 MC 14573 MC 14575	
			or	
	•		 other identification markings relating to devices complying with the abovementioned description 	0
266	ex85421930	*40	Thermocouple amplifier for instrumentation control at temperatures from 0 to 50°C, incorporating an alarm system, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			AD 594 AD 595	
•			or	
			 other identification markings relating to devices complying with the abovementioned description 	
267	ex85421930	•80	Amplifier with a programmable gain factor, in the form of a monolithic integrated analogue circuit contained in a housing	
			bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			PGA 102 PGA 202 PGA 203	

CN code	TARIC	Description	Rate of autonomous duty (%)
		or ·	,
		 other identification markings relating to devices complying with the abovementioned description 	0
271 ex8542195θ	•15	Current and voltage regulator of C-MOS technology, operating on a battery input voltage of 8,85 V or more but not exceeding 5,5 V or an unregulated input voltage of 7 V or more but not exceeding 20 V, providing a selectable output voltage of 3,3 V (±8,13 V) or 5 V (±8,28 V), in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		MAX 717 MAX 719 MAX 721 MAX 723 MAX 718 MAX 720 MAX 722	
•		07	•
	1	 other identification markings relating to devices complying with the abovementioned description 	e
272 ex85421958	•20	Voltage regulator with an input voltage range of 3 V or more but not exceeding 60 V and a quiescent current of 6 or 7 mA, comprising an internal 1,25 A, 2,5 A, 4 A or 5 A switch circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and tetters:	
·		LT 1070 LT 1071 LT 1170 LT 1171 LT 1172 LT 1271	
	÷	Or .	•
		 other identification markings relating to devices complying with the abovementioned description 	0
273 ex85421950	# 25	Voltage regulator, providing reverse battery protection, operating with an input voltage not exceeding 30 V and a quiescent current not exceeding 70 µA at zero load, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		LT 1129	
		or .	
		 other identification markings relating to devices complying with the abovementioned description 	0
274 ex85421950	*35	Voltage regulator, with a quiescent current not exceeding 75 mA and a dropout voltage not exceeding 0,6 V at an output current of 750 mA, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		TL750M TL751M	
		or	
	•	 other identification markings relating to devices complying with the abovementioned description 	0
275 ex85421950	+45	Voltage regulator, having an output voltage of 12 V (±3 %), a quiescent current not exceeding 18 mA and a dropout voltage not exceeding 22 V at an output current of 50 mA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the	
_		following combination of figures, and letters:	-
-		CS 8109 (7032FB)	
		or - other identification markings relating to devices complying	

	CN code	TARIC	Description	Rate	of	aut	onomou	s duly	(%)
276	ex85421950	\$ 55	Vollage regulator, with an input voltage of -15 V or more but not exceeding 60 V, an output voltage of 3,2 V or more but not exceeding 5,2 V, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:						
			LT 1142 LT 1149						
			or						
٠			- other identification markings relating to devices complying with the abovementioned description	θ					
277	e ₄ :85421950	•60	Voltage regulator with an input voltage range of 4,75 V or more but not exceeding 60 V and a quiescent current not exceeding 10 mA, comprising a 1 A switch circuit and an oscitlator with a fixed frequency of 52 kHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:						
			LM 1575 LM 2575						
			or						
			 other identification markings relating to devices complying with the abovementioned description 	0			•		
268	ex85421950	\$65	Voltage regulator, having an output voltage of 1 V or more but not exceeding 8 V, a typical quiescent current of 400 or 500 µA, a typical dropout voltage of 170 mV at an output current of 60 mA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:			•			•
	. *		TK 115 TK 116						
			or .			٠		•	
			 other identification markings relating to devices complying with the abovementioned description 	. ģ.					. •
278	ex85421950	#70	Voltage and current regulator of bipolar technology, capable of generating 3 ouput currents of respectively 7,5 mA, 50 mA and 750 mA at an ouput voltage of 5 V (±5 %), in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures:	:	•		•		
			34992						
	•		or .						
			other identification markings relating to devices complying with the abovementioned description	. 0					
269	ex85421950	+ 75	Voltage regulator, having an output voltage of 3,9 V (±3 %), a typical output current of 40 mA at an input voltage of 6 V and a typical operating current of 2,2 µA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	•	•		•		•
			SCI 7710V-KA			•			
		•	or						
	•		- other identification markings relating to devices complying					*	

	CN code	TARIC	Description	Rate of autonomous duty (%)
270	ex85421950		Adjustable voltage regulator, with a typical output current of 4 A, 6,5 A or 9,5 A at an input to output differential voltage of 5 V and a dropout voltage not exceeding 1,5 V, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			LT 1083 LT 1084 LT 1085	
•	•	٠	or .	
			- other identification markings relating to devices complying with the abovementioned description	8
282	ex85421960	+03	Control circuit of bipolar technology, for driving DC motors with brushes, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			BA 6109 BA 6209	•
			or	•
			 other identification markings relating to devices complying with the abovementioned description 	. 0
283	ex85421960	* 04	Three-phase DC motor control circuit of BiMOS technology, comprising a Hall effect threshold detection circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing:	* *
			 an identification marking consisting of or including the following combination of figures: 	
			1323454	
			or "	•
			 other identification markings relating to devices complying with the abovementioned description 	θ
284	ex85421960	# 05	Control circuit of C-MOS technology, for monitoring the voltage of microprocessors, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of	
			the following combinations of figures and letters: DS 1231 H 6060 MN 13802 MN 13821C	
			DS 1232 H 6061 MN 1381 MN 13822C H 6006 MN 1380 MN 13811 MN 1382C H 6052 MN 13801 MN 13812 V 7039	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	•
285	ex85421968	*07	Control circuit of bipolar technology, capable of driving N-MOS transistors, with a standby current not exceeding 3 µA, comprising an overvoltage shutdown circuit and a charge pump, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the	
			following combination of figures and letters:	
-	•		MC 33091	
		•	 other identification markings relating to devices complying with the abovementioned description 	
-286	ex85421960	∍ 08	Current-control circuit of bipolar technology, capable of	
9			driving a continuous output current of 2 A, having an output-error detection function, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	

CN code	TARIC	Description	Rate of	autonomous	duty ()	()
		PBD 3548				
·		or				
		 other identification markings relating to devices complying with the abovementioned description 	0			
287 ex85421960	*10	Circuit for driving current in a winding of linear motors or motors with rotating arms, of bipolar technology, operating with an output voltage not exceeding 45 V at an output current not exceeding 1,8 A, in form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:				
		PBL 3717 PBL 3770				
		or .				
		- other identification markings relating to devices complying with the abovementioned description	0			
288 ex85421960	*11	Circuit for driving linear motors or motors with rotating arms, of bipolar technology, working with an output voltage not exceeding 45 V at an output current not exceeding 1 A, comprising a clock generator, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures or of figures and letters:				
•		34993 PBL 3771 PBL 3772	٠.		•	
		or				•
		 other identification markings relating to devices complying with the abovementioned description 	. 0			
290 ex85421960	*15	Control circuit of C-MOS technology, capable of amplifying/inverting voltage levels to drive vertical lines of a charge-coupled (CCD) image sensor, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:				
		CXD 1267	•			
		or				
		- other identification markings relating to devices complying with the abovementioned description	θ.			•
291 ex85421960	*16	DC motor control circuit of bipotar technology, providing an output current of 2 A at an output saturation voltage of 3,2 V, comprising 3 TTL inputs, 4 transistors in a full bridge tonfiguration and an overvoltage shutdown circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	,			
	•	71004 MB				
		or		•		
		 other identification markings relating to devices complying with the abovementioned description 	6		•	
292 ex85421960	*17	Control circuit, capable of driving inductive or resistive loads, having an output current not exceeding 1,3 A at a supply voltage not exceeding 28 V, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	· ·			
_		71004 SB				
	•	or ·			•	
		 other identification markings relating to devices complying with the abovementioned description 	θ			
						1,

CN code	TARIC	Description	Raite of autonomous duty (%)
293 e>85421960	*18	Control circuit of BiMOS technology, capable of driving 2 power field-effect transistors (FETs), in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		LTC 1155	
		or	
		 other identification markings relating to devices complying with the abovementioned description 	0
294 ex85421960	*19	Voltage regulator control circuit, operating with a supply voltage of 6 V or more but not exceeding 30 V, providing an output voltage of 5 V (±0,1 V) at an output current of 220 µA, in form of a monolithic integrated analogue circuit contained in	
•		 a housing bearing: an identification marking consisting of or including the following combination of figures and letters: 	
		LT 1432	
•		or	
• .		 other identification markings relating to devices complying with the abovementioned description 	0
295 ex85421960	*20	Tachometer or tachometer and speedometer control circuit of BiNOS or bipolar technology, comprising a voltage regulating funktion, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		CS 8190 T 8557G TB 9226N TB 9228N TB 9233N	
		or	
		- other identification markings relating to devices complying with the abovementioned description	
296 ex85421960	#21	Control circuit of BiMOS technology, capable of driving inductive and resistive loads having 4 outputs with a current of 2 A or more but not exceeding 7,2 A, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures or of figures and letters:	•
		180984 HIP 8082	
•		or	
		 other identification markings relating to devices complying with the abovementioned description 	в
297 ex85421960	* 22	Control circuit of bipolar technology, capable of switching video and audio functions, with 5 inputs and 3 outputs, comprising 2 amplifiers and a mixer of luminance and chrominance signals, in form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		CX 1545 CXA 1855	•
•		or	
		 other identification markings relating to devices complying with the abovementioned description 	0
197 ex85421960	\$24	Quadruple fuel injector driver circuit of BiMOS technology, comprising a voltage regulator, an overvoltage detection circuit and an output status control circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	

	CN code	TARIC	Description	Rate of	autonomous	duty (%)
			7100050FSE				
			or .				
			 other identification markings relating to devices complying with the abovementioned description 	. 0-			
230	ex85421960	•25	Control circuit of bipotar technology, capable of driving solenoids, operating with a power supply current not exceeding 50 mA at a supply voltage not exceeding 7 V and a dissipation rate not exceeding 19 W, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:				
			71008SB				
			or		· ·		
			 other identification markings relating to devices complying with the abovementioned description 	в			
80	ex85421960	*26	Speedometer and odometer <u>drive</u> and control circuit, <u>whether</u> or not having amplification functions, comprising 4 frequency dividers, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	,			_
			TA 8906 TB 9207 TB 9208 TB 9212 TB 9230		•		
			or				
	•	١	 other identification markings relating to devices complying with the abovementioned description 	θ	•		
99	ex85421978	*50	Interface circuit of bipolar technology, capable of converting a differential input signal into a square wave output signal of the same frequency, comprising 4 signal sensor channels and a timer, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:				
			71901AB		•		
	•		or				
			 other identification markings relating to devices complying with the abovementioned description 	θ	•		
88	ex85421970	\$60	Interface circuit or interface circuit with control functions, for a local area network (LAN), in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:				-
	•		SMC 83C805				
			or				
		•	 other identification markings relating to devices complying with the abovementioned description 	8			
07	ex85421980	+01	Filter of C-MOS technology, with a programmable cut-off frequency of 4,5 MHz or more but not exceeding 25,2 MHz and a programmable frequency amplification not exceeding 9 dB, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	•			
			AD 896	· -		•	
			or			•	
		•	- other identification markings relating to devices complying with the abovementioned description	θ			

CN code	TARIC	Description	Rate of autonomous duty (%)
308 ex8542198	0 *02	Audio recording/reproducing circuit of C-MOS technology, capable of direct analogue storage of audio data with a duration of 10, 12, 16, 20, 45, 60, 75 or 90 seconds, comprising an electrically erasable, programmable, read only memory (E²PROM) with a storage capacity of 64 Kbits or more but not exceeding 1 Mbit, 3 amplifiers, an automatic gain control circuit and 2 filters, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		ISD 1012A	•
		or ·	
•	<i>;</i>	 other identification markings relating to devices complying with the abovementioned description 	8
309 ex8542198	9 993	Analogue signal microprocessor of bipolar technology, providing automatic gain control, read-signal processing and generation of head-positioning signals for magnetic heads in disc storage units, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		SN 28961	
		Or	
		 other identification markings relating to devices complying with the abovementioned description 	6
311 ex8542198	0 #86	Analogue modulator of C-MOS technology, having a dynamic range of 123 dB at 375 Hz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		CS 5323	
		or	
	• •	 other identification markings relating to devices complying with the abovementioned description 	0 .
312 ex8542198	9 *97	AM and FM receiver of bipolar technology, in the form of a monolithic integrated analogue circuit contained in a housing	
		 bearing: an identification marking consisting of or including one of the following combinations of figures and letters: 	
		CXA 1030 P CXA 1240 P	
	1	or	
		 other identification markings relating to devices complying with the abovementioned description 	? ·
313 ex8542198	• • • • • • • • • • • • • • • • • • • •	Switch unit of bipotar technology, for audio signals, having a distortion not exceeding 8,885 %, comprising 2 control units and 2 atternating switches, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
-	4	TK 15022 Z	
		Or .	
		- other identification markings relating to devices complying	-

CN code	TARIC	Description	Rate of autonomous duty (X)
314 ex85421980	9 10	Monolithic integrated analogue circuit of bipolar technology, for the overload protection of telephone exchanges, contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures or of figures and letters:	
		1515 TISP 1872F3 TISP 1882F3	
		or	
		- other identification markings relating to devices complying with the abovementioned description	0
315 ex85421980	#11	Programmable filter of bipolar technology, with a programmable cut-off frequency of 5 MHz or more but not exceeding 15 MHz and a programmable peak frequency and bandwidth, comprising a seven-pole filter and a differentiator, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of	
		the following combinations of figures and letters:	
		or	
		 other identification markings retating to devices complying with the abovementioned description 	0
316 ex85421980	*12	Dual-tone multi-frequency (DTMF) generator of C-MOS technology, capable of decoding 4-bit binary data and generating 16 tone pairs, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		TP 5088	
		or	
		 other identification markings relating to devices complying with the abovementioned description 	0
317 ex85421980	*13	Frequency converter of gallium arsenide (GaAs) semiconductor material, for the conversion of frequencies of 10,25 GHz or more but not exceeding 12,75 GHz to frequencies of 950 MHz or more but not exceeding 2050 MHz, in the folm of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		20070C AKD 12010 AKD 12575 AND 2001T4C AKD 12000 AKD 12011 AKD 2400 FMM 5103	
•		or	
		 other identification markings relating to devices complying with the abovementioned description 	0
318 ex85421980	+14	Voltage-to-frequency converter, comprising an amplifier, in the form of a monolithic integrated analogue circuit contained in a housing bearing:	
		 an identification marking consisting of or including one of the following combinations of figures and letters: 	
ť		VFC32 VFC100 VFC101	•
	·	 other identification markings relating to devices complying with the abovementioned description 	_ 0
319 ex85421980	*15	Current-to-voltage converter with an input current not exceeding 100 µA and an output voltage not exceeding -10 V, in the form of a monolithic integrated analogue circuit contained in a housing bearing:	
	•	 an identification marking consisting of or including the following combination of figures and letters: 	

CN code	TARIC	Description	· Rate of autonomous	duty (x)
		ACF 2101	•	
		or		
	•	 other identification markings relating to devices complying with the abovementioned description 	0	
320 ex85421980	•16	RMS-converter for computing the root mean square (RMS) value of wave-forms and converting this value to an equivalent direct current or an equivalent direct voltage, in the form of an monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:		
		AD 536 A AD 636 AD 637	•	
•		07		•
		 other identification markings relating to devices complying with the abovementioned description 	θ	
321 ex85421980	*17	Temperature transducer, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and tetters:		
		AD 590 AD 592		
		or		
		 other identification markings relating to devices complying with the abovementioned description 	. 0	
305 ex85421980	*18	Air pressure sensor, operating with a pressure range of 20 kPa to 105 kPA, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:		
		MPX 4100A		
		or		
		- other identification markings relating to devices complying with the abovementioned description	. 0	
322 ex85421980	•19	Image sensor consisting of a row of not more than 5000 photosensitive areas and a matrix linked to shift registers, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:		
	• .	LZ 2019 PD 3573 TCD 103 TCD 105 TCD 133 TCD 141		
		or		
		 other identification markings relating to devices complying with the abovementioned description 	0 .	
323 ex85421980	\$20	Intertine charge-coupled image sensor, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	· ·	
		ICX 018 ICX 022 ICX 038 PD 3732 ICX 021 ICX 024 ICX 039		i e
		or	,	•
	-	 other identification markings relating to devices complying with the abovementioned description 	- 0	

CN code	TARIC	Description	Rate of autonomous duty (%)
324 ex85421980	122	Detector for amplitude peaks in read/write signals of disc storage units, consisting of a differential amplifier with automatic gain control and a precision full-wave rectifier, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of	
		the following combinations of figures and letters:	
		32P3041 ML 8464	
		or	
		 other identification markings relating to devices complying with the abovementioned description 	0
325 ex85421980	#23	Voltage comparator, operating within a common voltage range of -12 V or more but not exceeding +12 V and a differential voltage range of -24 V or more but not exceeding +24 V and a response time not exceeding 6 ns, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		EL 2019 LT 1016	•
		or	
		 other identification markings retating to devices complying with the abovementioned description 	6
326 ex85421980	≢24	Amplifier/comparator of bipolar technology, for the amplification and comparison of phase/frequency signals from sensor inputs, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		CXA 1418 N	•
		or	•
		 other identification markings relating to devices complying with the abovementioned description 	0
327 ex85421980	*25	Half-bridgerectifier, consisting of 2 field effect transistors of MOS technology (MOSFETs), capable of driving inductive or capacitive loads with a nominal voltage of 50 V and a nominal current of 2 A, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		\$i9950DV	•
		or	
		 other identification markings retating to devices complying with the abovementioned description 	e :
328 ex85421980	*26	Voltage converter and regulator of bipolar technology, with a voltage loss not exceeding 1,6 V at an output current of 100 mA, operating with a supply voltage range of 3,5 V or more but not exceeding 15 V, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	•
		LT 1054	
		or	
		 other identification markings relating to devices complying with the abovementioned description 	- 0

	CN code	TARIC	Description	Rate of autonomous duty (%)
329	ex85421980	*27	5-channel voltage comparator for monitoring lamp-circuits, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			AD 22001	
			or	
		•	 other identification markings relating to devices complying with the abovementioned description 	0
330	ex85421988	*29	Local telephone network circuit of C-MOS technology, capable of tone generation and of switching, amplifying and decoding audio signals from not more than 2 external telephone lines and from not more than 12 internal telephone lines, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			SC 11390	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	0
331	ex85421980	•30	16-bit stereo encoder/decoder with C-MOS technology, having sample rates of 4 kHz or more but not exceeding 48 kHz, comprising a multiplexer, a digital-to-analogue converter, an analogue-to-digital converter, a mute circuit, a voltage reference circuit, a microphone-input, a loudspeaker-output and a headphone-output, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			AD .1849 CS 4215	•
			or	
			 other identification markings relating to devices complying with the abovementioned description 	. 8
332	ex85421980	*31	Audio circuit of C-MOS technology, with a dynamic range of 88 d8 or more, comprising 2 digital-to-analogue converters and 2 analogue-to-digitat converters, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or inctuding one of the following combinations of figures and letters:	
			AD 1848 CS 4231 CS 4248	
			or '	
		•	 other identification markings relating to devices complying with the abovementioned description 	0
333	ex85421980	•32	Speech-transfer circuit of bipotar technology, operating at a supply voltage of 2,3 V or more but not exceeding 22 V, providing continuously regulation of transmit and receive gain and a mute function, comprising 4 amplifiers, an internat voltage reference, 2 DC regulators and a power down function, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
	4		PBL 3850	•
_			or .	-
			 other identification markings relating to devices complying with the abovementioned description 	9

	CN code	TARIC	Description	Rate of autonomous duty (x)
334	ex85421980	#33	Video processing circuit of bipolar technology, for signals from a charge-coupled (CCD) image sensor, in the form of a monotithic integrated analogue circuit contained in a housing bearing:		
			 an identification marking consisting of or including one of the following combinations of figures and tetters: 	· · · · · · · · · · · · · · · · · · ·	
	•		AN 2014S CXA 1390 IR 3P69 IR 3P97 AN 2145FHP CXA 1391 IR 3P81A <u>IR 3V17</u> CXA 1310AQ CXA 1392 IR 3P92		
			00		
			 other identification markings retating to devices complying with the abovementioned description 	0 ,	
335	ex85421980	*34	Passive decoder of BiMOS technology, comprising a fixed matrix, a 7-kHz fitter, a noise-reducing circuit, a digital delay circuit and a memory-control circuit, in the form of a monotithic integrated analogue circuit contained in a housing bearing:		
			 an identification marking consisting of or including the following combination of figures and letters: 		
			LV 1000		
			00		
			- other identification markings relating to devices complying with the abovementioned description	0	
336	ex85421980	#35	Matrix decoder of BiMOS technology, comprising an adaptive matrix circuit, automatic-balance buffers, a noise generator and a control circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	•	•
			SSM 2125 SSM 2126		
		•	or	÷.	
			- other identification markings relating to devices complying with the abovementioned description	0	
337	ex85421980	*37	Serial 13-bit linear encoder/decoder of C-MOS technology, providing digital-to-analogue and analogue-to-digital conversion, comprising 2 sample and hold cicuits, a comparator/amplifier, a data-selector circuit, a successive approximation register, 2 shift registers, a sequence controller and a voltage reference circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the		
			following combination of figures and letters: MC 145402		
			or .		
		•	- other identification markings relating to devices complying with the abovementioned description	. 0	
338	ex85421980	•38	Encoder/decoder with pulse-code-modulation filter of C-MOS technology, operating with a +5 V single-power supply, comprising an analogue-to-digital converter and a digital-to-analogue converter, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	•	
			MC 145480	· <u>:</u>	
-			or		
			- other identification markings relating to devices complying with the abovementioned description	0	

CN code	TARIC	Description	Rate of autonomous duty (%)
339 ex85421980	*39	Encoder/decoder with pulse-code-modulation filter of C-MOS technology, with a dual-power supply and having a typical dissipation rate of 50 mM, comprising an analogue-to-digital converter and a digital-to-analogue converter, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the	
1		following combination of figures and letters: MC 145503	
	•	or	
		 other identification markings relating to devices complying with the abovementioned description 	
340 ex85421980	*40	Adaptive differentiated pulse-code-modulation circuit of C-MOS technology, for encoding/decoding data, capable of full duplex data-transmission between a channel with a transfer rate of 64 Kbits per second and a channel with a transfer rate of 16 Kbits, 24 Kbits, 32 Kbits or 64 Kbits per second, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		MC 145532	
		or ,	
		 other identification markings relating to devices complying with the abovementioned description 	0
341 ex85421980	#41 ·	Voltage reference circuit providing a typical output voltage of 2,5, 5, 7 or 10 V with a drift slope (output voltage temperature co-efficient) not exceeding 20 ppm/C, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		AD 580 AD 680 LT 1021 REF 102	•
		or .	
		- other identification markings relating to devices complying with the abovementioned description	0 .
342 ex85421980	+ 42	Voltage reference circuit with a reverse breakdown of 1,235 V (±4 mV) or 2,5 V (±20 mV), in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
	,	LT 1994	
		or	
		 other identification markings relating to devices complying with the abovementioned description 	
343 ex85421980	*43	Voice signal processing circuit of C-MOS technology, comprising an encoding circuit, a decoding circuit, a compression circuit, a decompression circuit and a modulator/demodulator (Modem) for full data-transfer at a speed of 1200 or 2400 bits/s, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		AK 2353 TC 35492	
_		or	·
•		- other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
344 ex85421980	*44	Modulator of bipolar technology, operating in the UHF band, for the conversion of audio and video signals, in a frequency range of 470 MHz or more but not exceeding 630 MHz, in the form of a monotithic integrated analogue circuit contained in a housing bearing:	
		 an identification marking consisting of or including one of the following combinations of figures and letters: 	•
		ALP 181 CXA 1333	
		or	
		- other identification markings relating to devices complying with the abovementioned description	θ
345 ex85421980	+45	Full-frame charge-coupled image sensor with single-phase clocking and not more than 1048576 photosensitive areas, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		TC 223	
,		or .	
		 other identification markings relating to devices complying with the abovementioned description 	0
346 ex85421980	#46	Encoder/decoder with pulse-code-modulation filters of C-MOS technology, capable of voice digitisation and reconstruction at a speed of 64 Kbits/s or more but not exceeding 2048 Kbits/s, with a single power supply of 5 V, a power dissipation not exceeding 37 mV in operating mode and not exceeding 3 mV in power down mode and capable of automatically entering in power down mode with clock stop, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		7508 B 7509 B	•
		or	. •
		 other identification markings relating to devices complying with the abovementioned description 	
347 ex85421980	#4?	Programmable diode array, consisting of 14 individual diodes and a rectifier, of gallium arsenide (GaAs) semiconductor material, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		166010 166011	
		or	•
		 other identification markings relating to devices complying with the abovementioned description 	. 0
348 ex85421980	*48	Phase-locked loop (PLL) circuit of bipolar technology, comprising an oscillator, a frequency and/or phase detector, 4 prescalers/counters, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		SN 28967	•
		or .	_
-		 other identification markings relating to devices complying with the abovementioned description 	. · · · · · · · · · · · · · · · · · · ·

	CN code	TARIC	Description	Rate of autonomous duty (X)
349	ex85421980	≢49	Video processing circuit of bipolar technology, for colour or tuminance signals, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			CXA 1207 CXA 1208	
			 other identification markings relating to devices complying with the abovementioned description 	
			These devices are for use in the manufacture of cameras of the 8 mm type (a)	0
350	ex85421980	*50	FM-band receiver of bipolar technology, providing FM-signal demodulation, comprising at least a mixer, an intermediate frequency (IF) amplifier, a limiter amplifier and a data sticer, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			MC 13156 MC 13158	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	θ
351	ex85421980	* 51	Audio compression/decompression circuit of bipotar technology, providing a compression and an expansion mute function, operating at a supply voltage of 3 V or more but not exceeding ? V, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			MC 33111	
			or	1
	·		 other identification markings relating to devices complying with the abovementioned description 	0
352	ex85421980	* 52	Transmitter/receiver of BiMOS technology, capable of data transmission at a rate per second of 10 Mbits, comprising 6 transmitters and 6 receivers, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			MC 34858	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	0
353	ex85421980	# 53	Converter/amplifier of bipolar technology, with an output level of 22 dBm at a frequency of 900 MHz and an input level of -6 dBm, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
÷			HP 3001	
			07	•
			 other identification markings relating to devices complying with the abovementioned description 	0

	CN code	TARIC	Description	Rate of autonomous duty (X)
3,54	ex85421980	354	Current transmitter of bipolar technology, with an output current of 4 mA or more but not exceeding 20 mA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:		
			XTR 103 XTR 104		
			or		
			 other identification markings relating to devices complying with the abovementioned description 	8	
355	ex85421980	#55	Voltage-to-current converter of bipotar technology, with a selectable input voltage range and a power supply voltage of 13,5 V or more but not exceeding 40 V, comprising a current transmitter and a voltage reference circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing:		
	•		 an identification marking consisting of or including the following combination of figures and letters: 		
			XTR 110		
			10		
			 other identification markings relating to devices complying with the abovementioned description 	0	
356	ex85421980	₹ 56	Voltage converter of C-MOS technology, capable of transforming an input voltage level not exceeding 5 V at an input current not exceeding 0,1 µA into an output voltage not exceeding 15 V at an ouput current not exceeding 1 mA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:		
			LR 36683N	•	
			10		
•	•		 other identification markings relating to devices complying with the abovementioned description 	8	
357	ex85421980	•57	Frequency converter of gallium arsenide (GaAs) semiconductor material, with a conversion gain of 5 dB, capable of converting an input frequency of 50 MHz or more but not exceeding 860 MHz into an output frequency of 1220 MHz, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:		
			AND 7000S3C		
			or	•	
			 other identification markings relating to devices complying with the abovementioned description 	e .	
358	ex65421980	# 58	Frequency converter of gattium arsenide (GaAs) semiconductor material, with a conversion gain of 6 dB, capable of converting an input frequency of 1220 MHz into an output frequency of 45. MHz, comprising an oscillator, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	•	
			* AND 700183C		
		•	or	-	
-			 other identification markings relating to devices complying with the abovementioned description 	0	

	CN code	TARIC	Description	Rate of autonomous duty (X)
359	ex85421986	*59	Frequency converter of gallium arsenide (GaAs) semiconductor material, with a typical conversion gain of 20, 26 or 50 dB, capable of converting an input frequency of 500 MHz or more but not exceeding 2,5 GHz into an output frequency of 30 MHz or more but not exceeding 500 MHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters:	
			TQ 9201 TQ 9202 TQ 9203	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	θ
360	ex85421980	*60	Analogue communication circuit, capable of data conversion and signal transfer, comprising a serial input/output port for a digital signal processor (DSP), a 16-bit analogue-to-digital converter, a 16-bit digital-to-analogue converter and a clock generator, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			AD 28MSP01	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	в
361	ex85421980	•61	FM receiver of bipolar technology, capable of operating at an input frequency rage of 200 MHz, with an FM signal demodulating function, comprising at least 2 mixers, an oscillator, a diode and a Receive Signal Strengh Indicator (RSSI), in the crm of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			MC 13135 MC 13136	
			OF	•
			 other identification markings relating to devices complying with the abovementioned description 	0
362	ex85421980	≇62	AM or FM transmitter of bipolar technology, with AM or FM signal modulation function, comprising 3 amplifiers, a prescaler and 2 oscillators, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			MC 13175 MC 13176	
			ог	
		•	- other identification markings relating to devices complying with the abovementioned description	b
363	ex85421980	#63	Video processing circuit of bipolar technology, for colour and synchronisation signals, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			CXA 1213BS CXA 1587	
			or	•
, . .			 other identification markings relating to devices complying with the abovementioned description 	θ .

	CN code	TARIC	Description	Rate of autonomous duty (%)
364	ex85421980	•64	AM-band receiver of bipolar technology, providing conversion of radio frequency (RF) into dual intermediate frequency (IF) and detection of audio frequency, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the	
			following combination of figures:	•
		•	or	
			 other identification markings relating to devices complying with the abovementioned description 	θ .
365	ex85421980	* 65	Frequency converter of gallium arsenide (GaAs) semiconductor material, with a conversion gain of 6 dB, capable of converting an input frequency of 950 MHz or more but not exceeding 2050 MHz into an output frequency of 480 MHz, comprising an oscillator, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			ADC 20013	•
			or	•
			 other identification markings relating to devices complying with the abovementioned description 	0
366	ex85421980	+66	Frequency synthesiser of C-MOS technology, operating with an input frequency not exceeding 60 MHz and a DC supply voltage not exceeding 10 V, comprising a phase-locked toop (PLL) circuit and a programmable 14-bit reference counter, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	•
	•		HC 145158 HC 1451 <u>62</u>	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	8
367	ex85421980	•67	FM-band receiver of BiMOS technology, comprising a compression circuit, a decompression circuit, 2 mixers, 2 phase-tocked toop (PLL) circuits, an intermediate frequency (IF) amplifier, a receive signal strength indicator (RSSI), a serial interface circuit and a supply voltage detection circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
	•		MC 13108	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	.0
368	ex85421980	* 69	FM-band receiver/demodulator of bipolar technology, comprising 2 conversion mixers, a data slicer and 6 amplifiers, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	· · ·
			10X6	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	θ

	CN code	TARIC	Description	Rate of autonomous duty (%)
369	ex85421980	≇7 8	FM-band transmitter/receiver of bipolar technology, comprising 2 conversion mixers, a prescaler and 4 amplifiers, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			10X5	
		٠.,	or	
			 other identification markings relating to devices complying with the abovementioned description 	θ
370	ex85421980	•71	Acceleration measurement circuit, comprising a capacitif sensor, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			ADXL50	
			or	•
			 other identification markings relating to devices complying with the abovementioned description 	0
371	ex85421980	•72	Audio noise reduction circuit of bipolar technology, having an input voltage not exceeding 18 V, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			LM 1894 TK 18654	
			or	
			- other identification markings relating to devices complying with the abovementloned description	θ
372	ex85421980	173	Signat processing circuit of C-MOS technology, providing analogue signal filtering and gain control, comprising a dual-tone multifrequency (DTMF) transmitter and a DTMF receiver, and a modulator/demodulator (Modem), in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			SC 11370	•
			or	
			- other identification markings relating to devices complying with the abovementioned description	0
373	ex85421988	●74	Filter network only consisting of 16 resistors, 16 capacitors and 16 diodes, in the form of a monotithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			USRC 1002	
		•	or	
			 other identification markings relating to devices complying with the abovementioned description 	0
374	ex85421980	•75	Encoder/decoder of C-MOS technology, for base-band and voice-band frequencies, providing data conversion, comprising a modulator for digital signals, analogue-to-digital converters, digital-to-analogue converters, amplifiers and filters, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and tetters:	-
			AD 7015	

CN code	TARIC	Description	Rate of autonomous duty (%)
		- other identification markings relating to devices complying with the abovementioned description	0
375 ex85421980 .	* 76	Voltage converter of C-MOS technology, capable of inverting, doubling, dividing or multiplying input voltages, operating at a supply voltage range of 1,5 V or more but not exceeding 10 V, inthe form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		ICL 7660 MAX 1044	
		or	
		- other identification markings relating to devices complying with the abovementioned description	8
376 ex85421986	\$77	Signal processing circuit of C-MOS technology, providing delay of scanning periods for horizontal image lines of a charge-coupled (CCD) image sensor, comprising a clockgenerator, a clamp circuit and a sample and hold circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing:	
		 an identification marking consisting of or including one of the following combinations of figures and letters: 	
		CXL 1506 MSM 6965 RS MSM 7401 RS	
		or	*
•		 other identification markings relating to devices complying with the abovementioned description 	0
302 ex85421980	* 78	Audio signal processing circuit of bipolar technology, providing input/output mode selection, comprising 2 voltage controlled oscillators, 2 phase-locked loop (PLL) circuits, an automatic gain control circuit, an audio noise reduction circuit, a mute circuit and amplifiers, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the	
		following combination of figures and letters:	
		or	
		 other identification markings relating to devices complying with the abovementioned description 	0
303 ex85421980	*79	Frequency converter of bipolar technology, operating with a frequency range of 800 MHz to 900 MHz and with an input level not exceeding -6 dBm, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		CXA 1851N	
	•	or .	
		 other identification markings relating to devices complying with the abovementioned description 	9
304 ex85421980	*80	6-channet DC-to-DC converter of BiMOS technology, in the form of a monolithic integrated anatogue circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
•		MB 3799	
-		or	
		 other identification markings relating to devices complying with the abovementioned description 	θ

306 ex85421980	*81	Voltage detection circuit, capable of resetting external circuits, in the form of a monolithic integrated analogue circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters: M 51957 M 51958	
			•
		or	
		 other identification sarkings relating to devices complying with the abovementioned description 	
377 ex85422030	* 59	12-bit analogue-to-digital converter of C-MOS technology, comprising a sample and hold amplifier having a dynamic performance of 1 MHz per second or more, in the form of a hybrid integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		ADS 112 ADS 117	•
		or	
		 other identification markings relating to devices complying with the abovementioned description 	0
378 ex85422950	*30	Amplifier with a programmable gain factor, in the form of a hybrid integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	•
		3606 C	
		Or .	
-		 other identification markings relating to devices complying with the abovementioned description 	8
379 ex85422050	140	Amplifier with an input power of 1 mW and an output power <u>not</u> <u>exceeding</u> 3,5 W at a frequency range of 890 MHz or more but not exceeding 915 MHz or of 1718 MHz or more but not exceeding 1785 MHz, in the form of a hybrid integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		FA 01314 XHW 903	
	•	or and the second secon	
		 other identification markings relating to devices complying with the abovementioned description 	6
29b ex 8542 2050	•70	Amplifier of bipolar technology, operating within a frequency range of 800 MHz to 950 MHz, with at least one of the following characteristics:	
,	•	- a) an output power of 12,5 W at an input power of 100 mW, - b) an output power of 20 W at an input power of 200 mW,	
		in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of	
		the following combinations of figures and letters:	e e e e e e e e e e e e e e e e e e e
		or	
•		 other identification markings retating to devices complying with the abovementioned description 	0
382 ex85422080 	+50	Voltage and current regulator, having an output voltage not exceeding 850 V at a drive current not exceeding 8,7 A, comprising a power transistor and a control circuit with an oscillator, in the form of a hybrid integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	-

	CN code	TARIC	Description	Rate of autonomous duty (%)
			or	
			 other identification markings relating to devices complying with the abovementioned description 	θ
381	ex85422080	•60	Voltage regulator with a nominal input <u>operating</u> voltage of 276 V, an input current not exceeding 8 A and a <u>n</u> operating frequency not exceeding 200 kHz, in the form of a hybrid	
	*		integrated circuit contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
			MA 2810 MA 2820 MA 2830	
			or	
			 other identification markings relating to devices complying with the abovementioned description 	0
384.	ex85422088	•70	Voltage and current regulator, having an input voltage not exceeding 35 V and a quiescent current not exceeding 100 µA, comprising a field-effect transistor (FET) with a drain current not exceeding 32 A, in the form of a hybrid integrated circuit contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		,	STR M6523	
		•	or	
			 other identification markings relating to devices complying with the abovementioned description 	
71	ex85428000	•94	Dual silicon zener diode, with a zener voltage of 11 V or more but not exceeding 13 V and a dissipation rate not exceeding 200 mW, in the form of a microassembly contained in a housing	0
383	ax85428000	•05	Quintuple field-effect transistor (FET), having a drain-to-source breakdown-voltage of 100 V or more, operating with a continuous drain current not exceeding 5 A, and with a dissipation rate not exceeding 35 W, in the form of a microassembly contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
			SLA 5021	*
			or	,
			 other identification markings relating to devices complying with the abovementioned description 	0
380	ex85438095	+21	Amptifier of bipolar technology, operating within a frequency range of 68 MHz to 88 MHz, with an output power of 5°W at an input power of 1 mW, consisting of active and passive elements mounted on a printed circuit, contained in a housing	
		•	 bearing: an identification marking consisting of or including the following combination of figures and letters: 	
			NHW <u>185</u>	
		,	07	•
			 other identification markings relating to devices complying with the abovementioned description 	· • • • • • • • • • • • • • • • • • • •
29a	ex85438095	•23	Amplifier of bipolar technology, operating within a frequency range of 800 MHz to 950 MHz, with at least one of the following characteristics:	
		•	- a) an output power of 1,41 W at an input power of 5 mW, - b) an output power of 2 W at an input power of 1 mW,	-
		•	- c) an output power of 3,2 W at an input power of 2 mW,	
		· .	- d) an output power of 3,5 W at an input power of 188 mW,	
			a, on onchos homes of 3's war an subar bones of 100 mm.	

- e) an output power of 6 W at an input power of 100 mW,

CN code	TARIC	Description	Rate of autonomous duty (%)
		- f) an output power of 14 W at an input power of 1 or 180 mW,	
		consisting of active and passive elements mounted on a printed circuit, contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	
		a)MHW 9002 c)PHW 902 e)SHW 5115 f)MHW 914 b)MHW 803 d)MHW 953 e)XHW 5115 f)MHW 915	• *
	•	or	,
		 other identification markings relating to devices complying with the abovementioned description 	
.31 ex854380	995 •25	Amplifier of gallium arsenide (GaAs) semiconductor material, operating with a frequency range of 1710 MHz to 1785 MHz,	
	• • • • • • • • • • • • • • • • • • • •	with an output power of 3 M at an input power of 1 aM, consisting of active and passive elements sounted on a printed circuit, contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
		PHW 9012	
		or , a	•
		 other identification markings relating to devices complying with the abovementioned description 	 0
32 ex85438	995 #27	Amplifier of bipolar technology, operating within a frequency range of 136 MHz to 174 MHz, with an output power of 7 W at an input power of 1 mW, consisting of active and passive elements mounted on a printed circuit, contained in a housing bearing: - an identification marking consisting of or including the	
		following combination of figures and letters:	
	·	ины 607	
		or. - other identification markings relating to devices complying with the abovementioned description	9
390 ex854384	995 •51	Temperature compensating frequency oscillator with a nominal frequency of 12,8 MHz and operating at a supply voltage of 3 V-(18,3 V), comprising a printed circuit on which are mounted at least a piezo-electric crystal and an adjustable capacitor, contained in a housing with not more than 5 connections and bearing: - an identification marking consisting of or including the	
		following combination of figures and letters:	
		TX 02603	
		or	
	•	 other identification markings retating to devices complying with the abovementioned description 	8
393 ex85439	990 •10	Dust field-effect transistor with at least one of following characteristics: - a) of P-MOS technology (Dust P-MOSPET), having a drain-to-source breakdown-voltage of -20 V, operating with a	
		drain-current not exceeding 9,2 A and with a dissipation rate not exceeding 2 W, - b) of M-MOS (including H-MOS) technology (Duat N/H-MOSFET), having a drain-to-source breakdown-voltage of 20 V or more, operating with a drain-current not exceeding 3,5 A and with a dissipation rate not exceeding 2 W, contained in a housing bearing: - an identification marking consisting of or including one of	
 .		the following combinations of figures:	
		a)9947 a)99 <u>53</u> b)9956 b)9959	
		or - other identification markings relating to devices complying	
		with the abovementioned description	θ

	CN code	TARIC	Description	Rate of autonomous duty (%)
394	ex85446818	* 19	Electric cable, insulated with plastic, for a voltage of 28 kV or more but not exceeding 32 kV, fitted with an anode in a rubber cap at one end and with a metallic connector at the other end	÷
388	ex85480000	•94	Optical unit, consisting of a laserdiode and a photodiode, operating at a typical wavelength of 635 or 670 na	θ ,
391	ex85480000	. •95	Infrared signal receiver unit, consisting of a photodiode and at least an amplifier in the form of a monolithic integrated circuit, contained in a housing bearing: - an identification marking consisting of or including one of the following combinations of figures and letters:	•
			GP1U58XB SBX 1618	
			 other identification markings relating to devices complying with the abovementioned description 	θ
392	ex85480000	*96	Fibre-optic cable transmitter unit, consisting of a tight-emitting diode (LED) operating at a wavelength of 660 nm (±30 nm) and a control circuit in the form of a monotithic integrated circuit, contained in a housing bearing: - an identification marking consisting of or including the following combination of figures and letters:	
•			GP1F32T	•
			 other identification markings relating to devices complying with the abovementioned description 	0
397	ex90019090	●30	Lens of plastic, unmounted, having a focal length of 3,86 mm (±0,1 mm) and with a diameter not exceeding 8 mm, for use in the manufacture of compact disc players (a)	C
396	ex90019090	#4 0	Optical fibre plate, with an external diameter not exceeding 38 mm, for use in the manufacture of screens and photocathodes for image intensifiers (a)	6
395	e×90019090	•50	Lens, mounted, having a fixed focal length of 3,8 mm (±0,19 mm) or 8 mm (±0,4 mm), with a relative aperture of F2.0 and a diameter not exceeding 33 mm, for use in the manufacture of charged-coupled (CCD) cameras (a)	8
400	90138030		Liquid crystal <u>devices, other than active matrix liquid</u> crystat devices	θ .
482	ex90138090	* *10	Polarisation insensitive fibre-optic isolator, operating at a wavelength of 1300, 1480 or 1550 nm, contained in a cylindrical housing with 2 optical fibre cables	8

⁽a) Control of the use for this special purpose shall be carried out pursuant to the relevant Community provisions.

FINANCIAL RECORD

- 1. Budget line concerned: Chap. 12 Art. 120
- 2. <u>Title of the tariff measure concerned</u>: Proposal for a Council Regulation, temporarily suspending the autonomous Common Customs Tariff duty on certain industrial products (in the microelectronics and related sectors)
- 3. Legal basis: Art. 28 of the Treaty
- 4. Objective: Suspension of CCT duties on the abovementioned products
- 5. Cost of this tariff measure:
 to be borne by the EC-budget:

Difficult to estimate, due to lack of adequate and precise Community statistics.

Hence, on the basis of the data supplied by various Member States, the cost of the operation can be indicated as follows:

- 1. Cost of the exercise for the previous round:
 - 1.7.94-31.12.94:

75 700 000 ECUS

2. <u>Cost of the current exercise (1,1.95-30.6.95)</u>

95 000 000 ECUS.

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DOCUMENTS

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